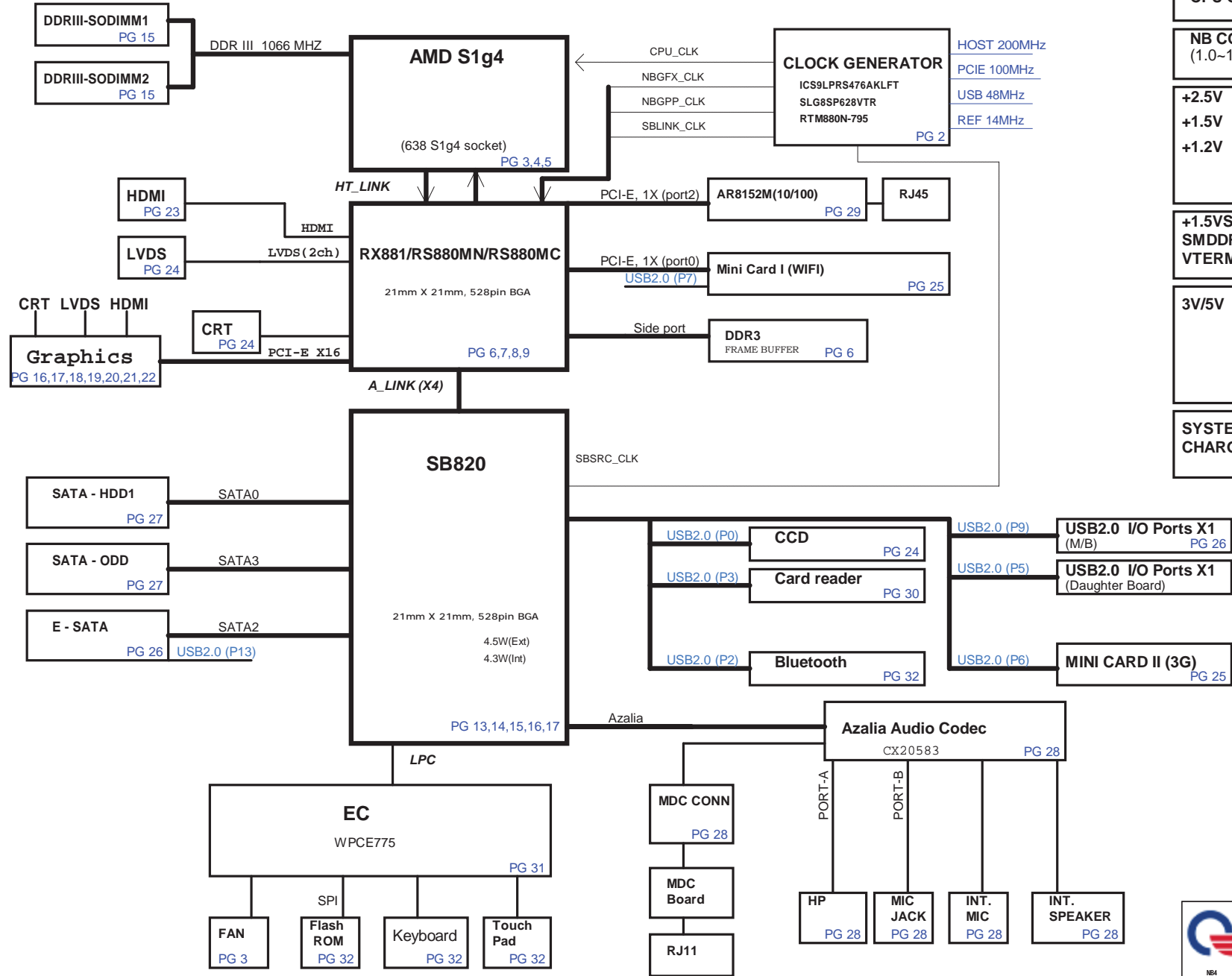


TE3 BLOCK DIAGRAM




PCB STACK UP

- LAYER 1 : TOP
- LAYER 2 : GND1
- LAYER 3 : IN1
- LAYER 4 : IN2
- LAYER 5 : VCC
- LAYER 6 : BOT

- CPU CORE
- NB CORE (1.0~1.2V)
- +2.5V
- +1.5V
- +1.2V
- +1.5VSUS
- SMDDR
- VTERM
- 3V/5V
- SYSTEM CHARGER

Daughter Board

- MMB Board
- Audio & USB Board
- Touch Pad Board (with FP & w/o FP)
- LED Board
- MDC Board

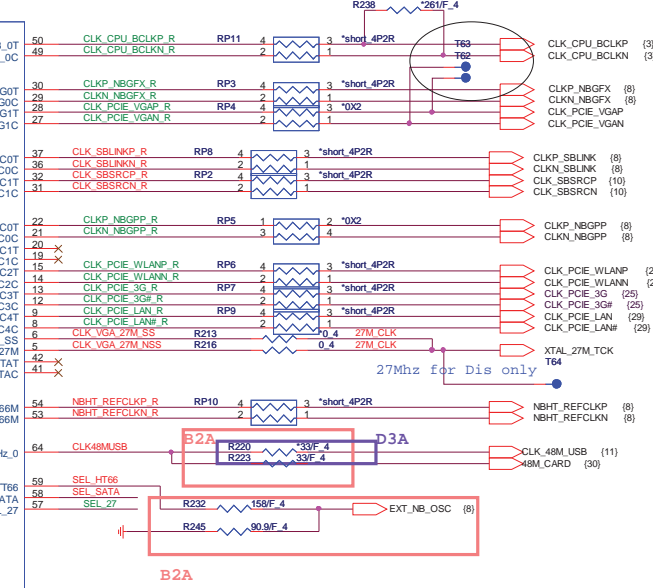
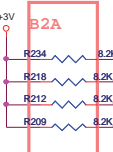
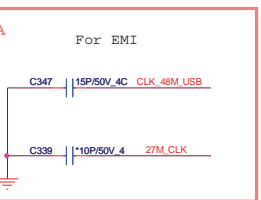
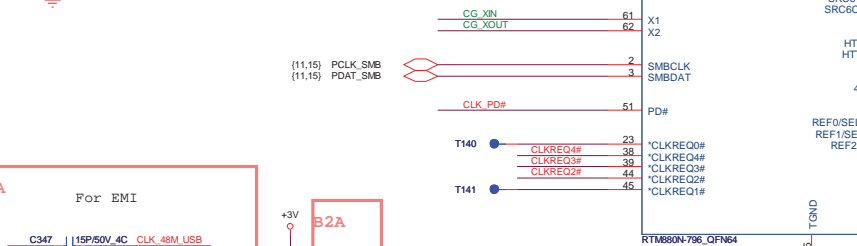
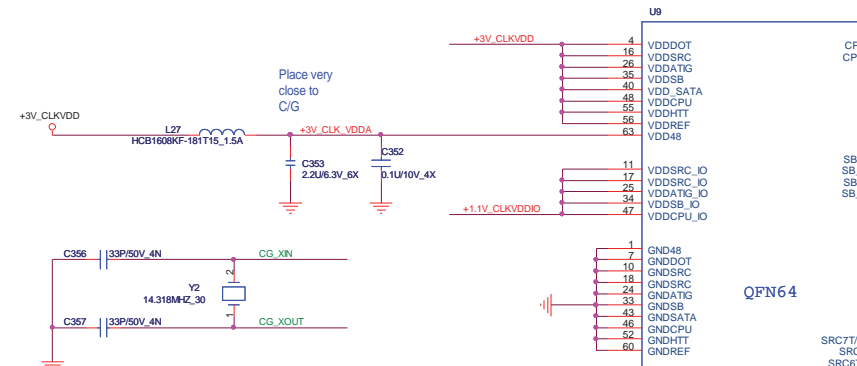
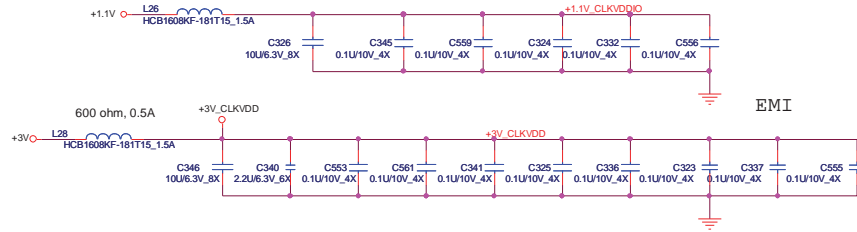
	Quanta Computer Inc.		
	PROJECT : TE3		
	Size	Document Number	Rev
BLOCK DIAGRAM		1A	
Date: Tuesday, March 02, 2010		Sheet 1 of 43	

<http://laptop-motherboard-schematic.blogspot.com/>

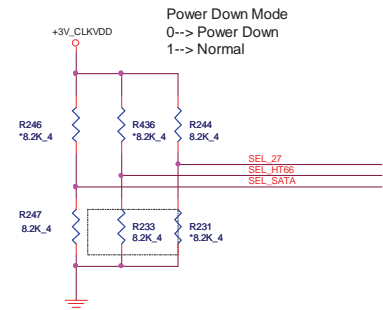
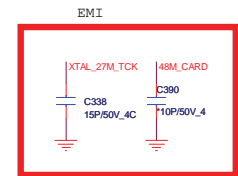
CLOCK GENERATOR

CLOCKS name	Discrete	Clock pin function
NBFX_CLKP NBFX_CLKN	RP48 STUFF	to NB for VGA reference clock
EXT GFX_CLKP EXT GFX_CLKN	RP47 STUFF	to Park-S3 external reference clock -Discrete only
SBLINK_CLKP SBLINK_CLKN	RP43 STUFF	to NB for AC-LINK reference clock
CLK_VGA_27M_SS CLK_VGA_27M_NSS	R213,R215 STUFF	To Park-S3 27Mhz - Discrete only

Need check the net name for the short pad



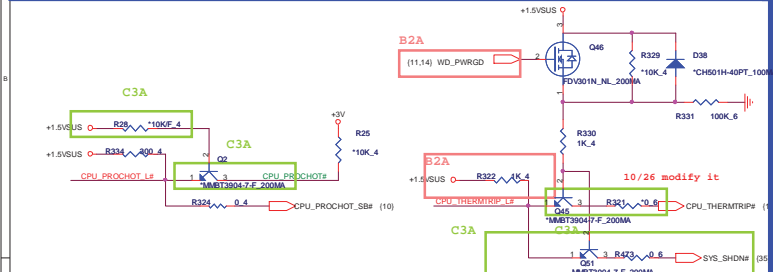
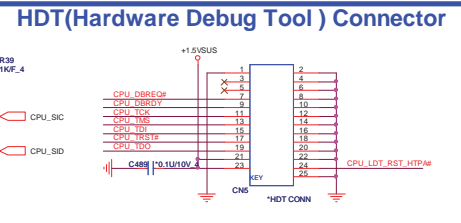
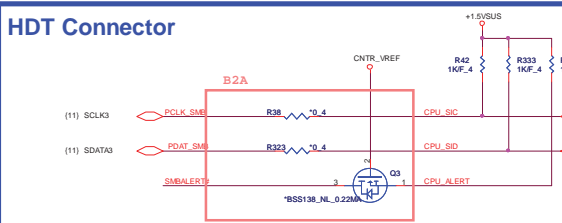
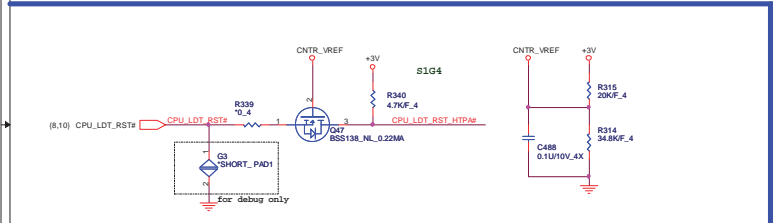
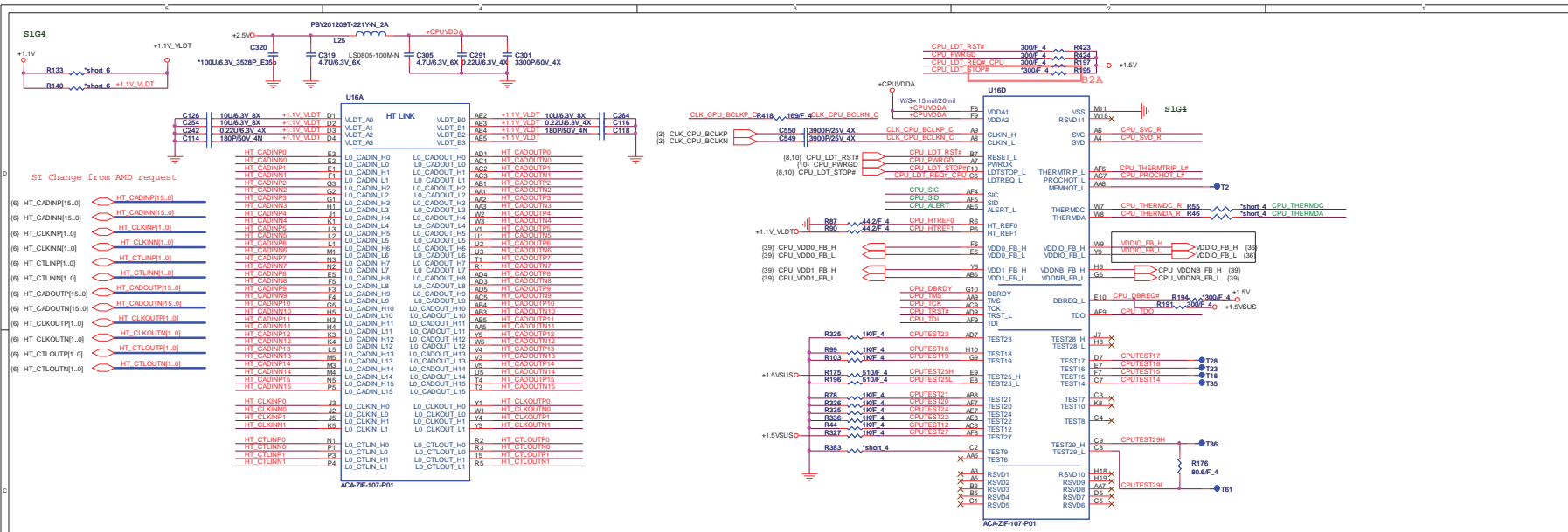
- to CPU
- to NB
- TO VGA CARD
- EXTERNAL MODE to NB-AC-LINK
- EXTERNAL MODE to SB
- Reserve ONLY
- to WLAN
- to 3G
- to LAN
- to VGA

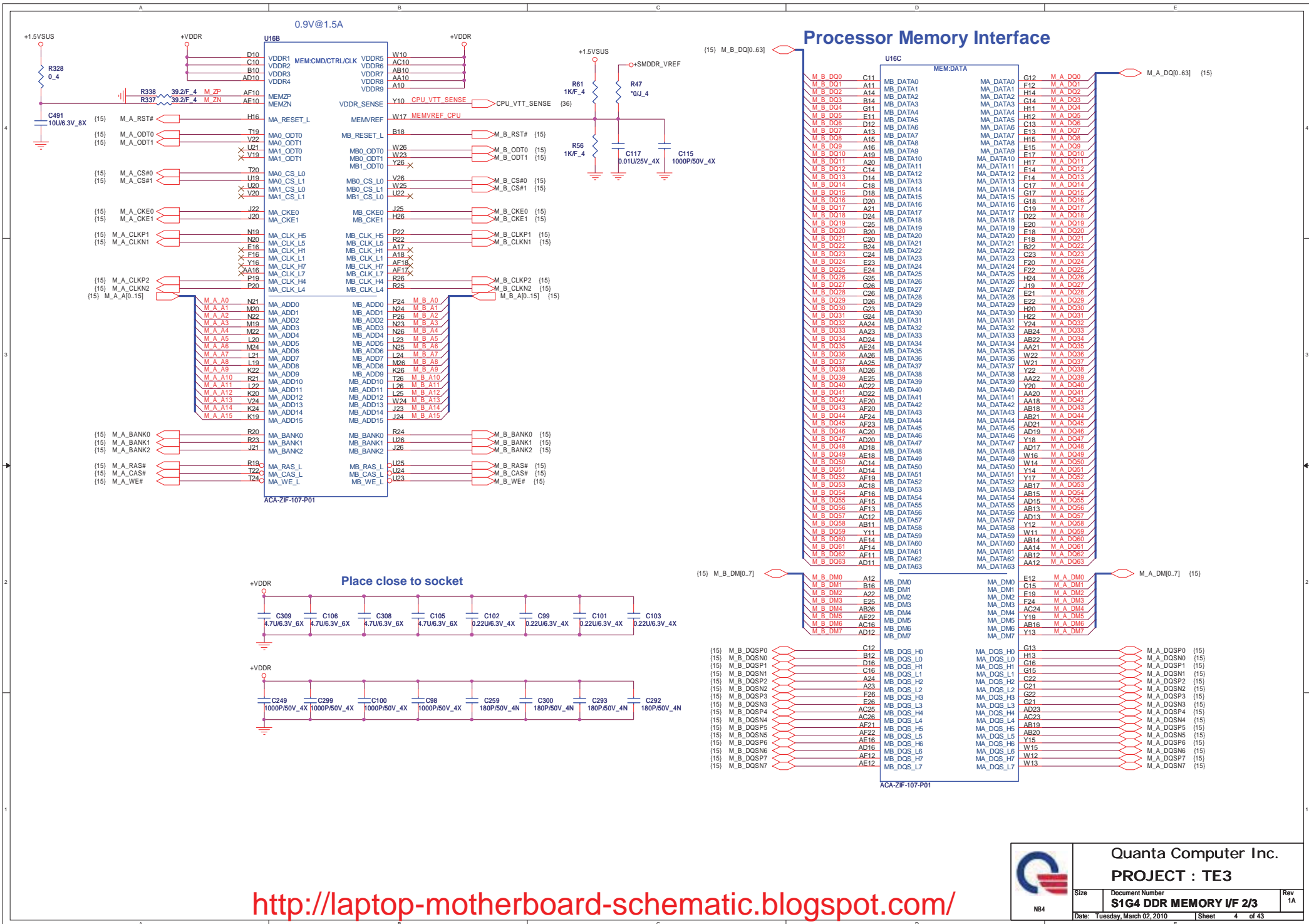



SEL	Value	Function
SEL_HTT66	1	66 MHz 3.3V single ended HTT clock
SEL_HTT66	0*	100 MHz differential HTT clock
SEL_SATA	1	100 MHz non-spreading differential SRC clock
SEL_SATA	0*	100 MHz spreading differential SRC clock
SEL_27	1*	27MHz non-spreading singled clock
SEL_27	0	100 MHz spreading differential SRC clock

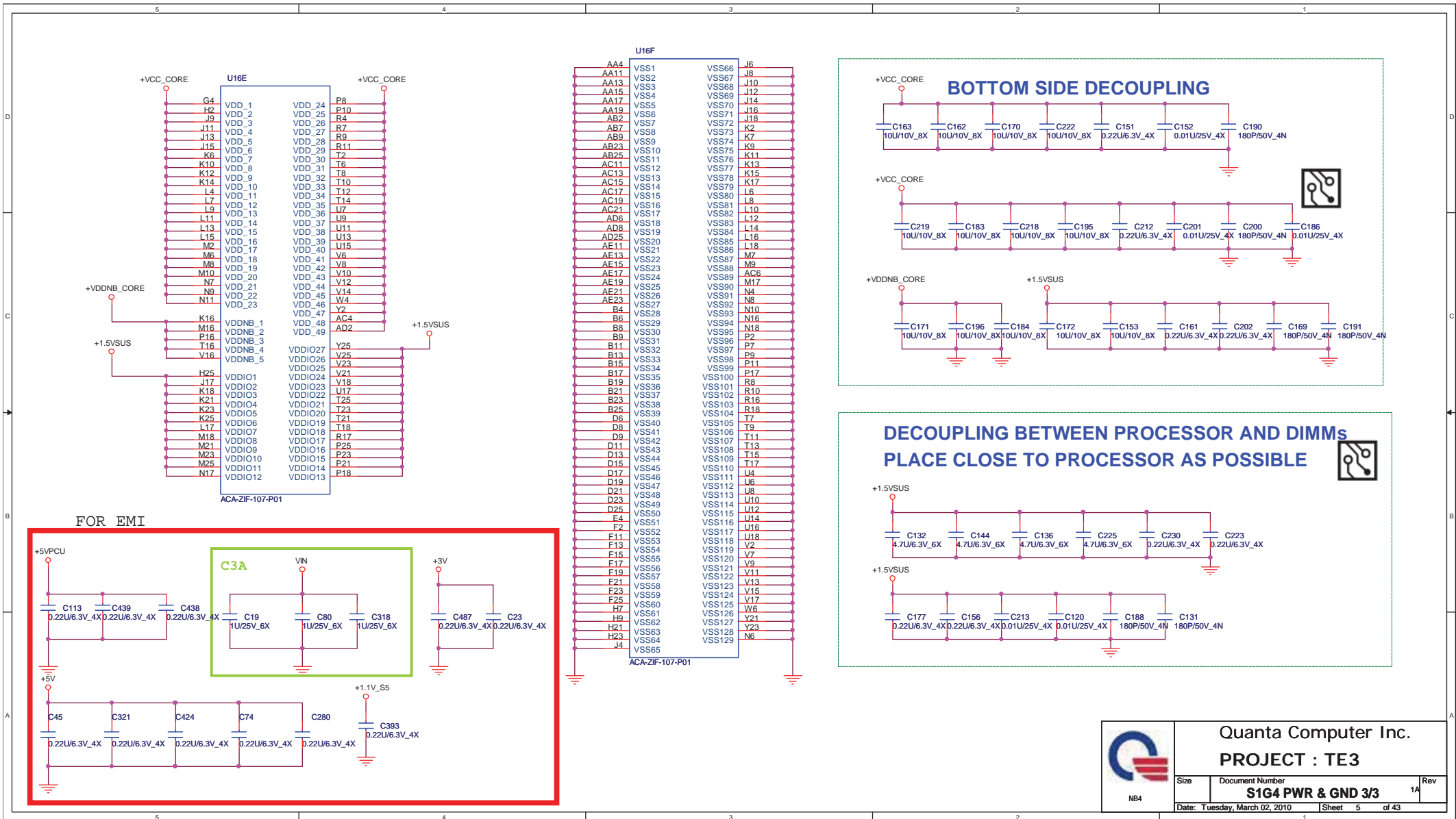
<http://laptop-motherboard-schematic.blogspot.com/>

Quanta Computer Inc. PROJECT : TE3			
Size	Document Number	Rev	
	Clock Generator	1A	
Date:	Thursday, March 04, 2010	Sheet	2 of 43




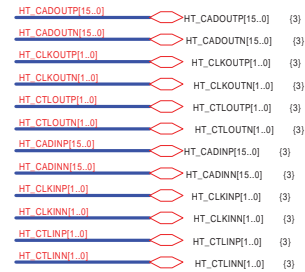
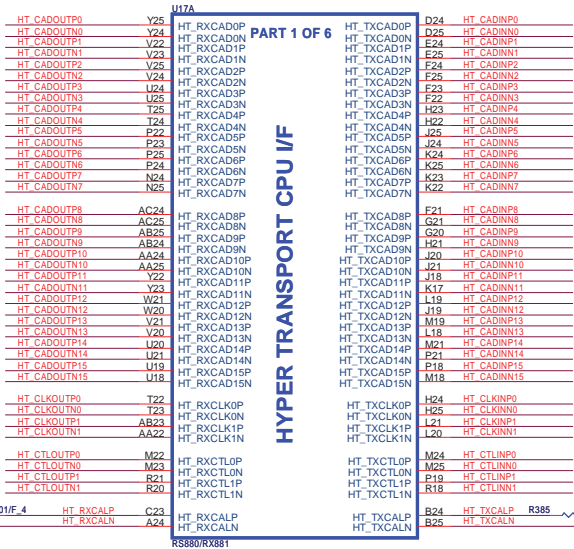


		Quanta Computer Inc.	
		PROJECT : TE3	
Size	Document Number	Rev	1A
NB4		S1G4 DDR MEMORY I/F 2/3	
Date:	Tuesday, March 02, 2010	Sheet	4 of 43



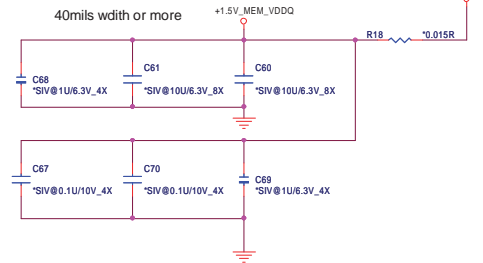
<http://laptop-motherboard-schematic.blogspot.com/>

 NB4	Quanta Computer Inc. PROJECT : TE3		
	Size	Document Number S1G4 PWR & GND 3/3	Rev 1A
Date: Tuesday, March 02, 2010		Sheet 5 of 43	

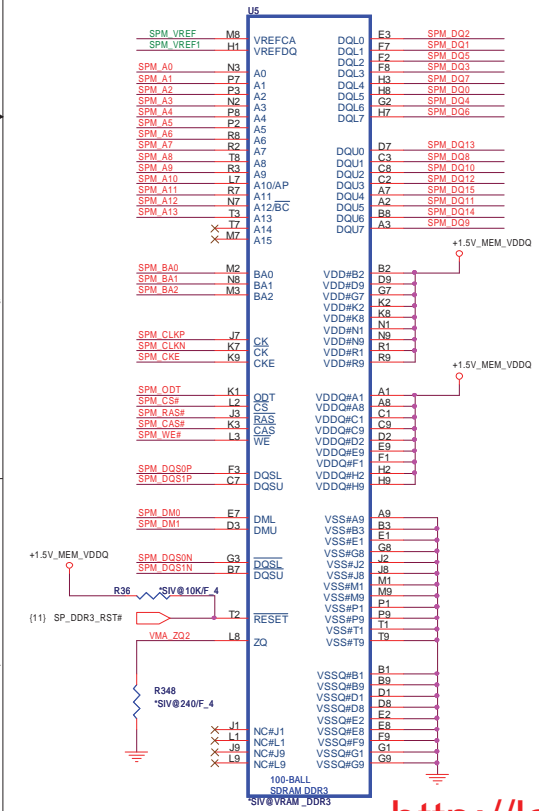
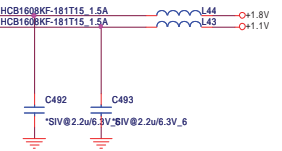
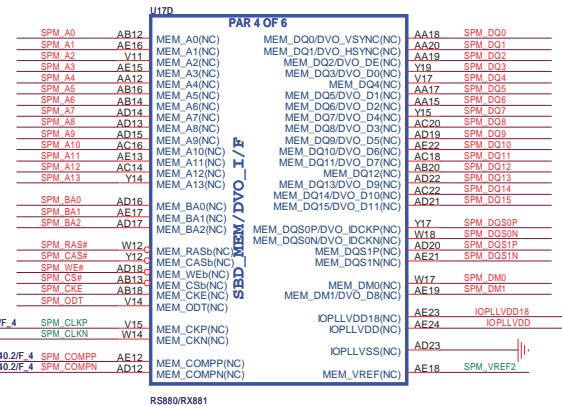


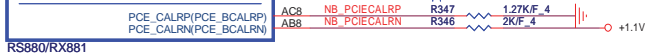
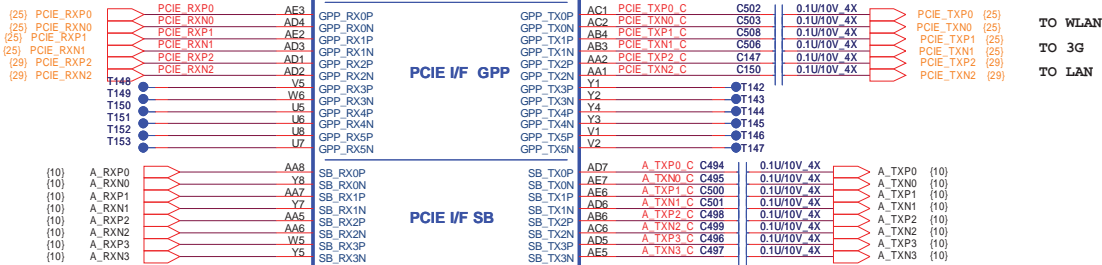
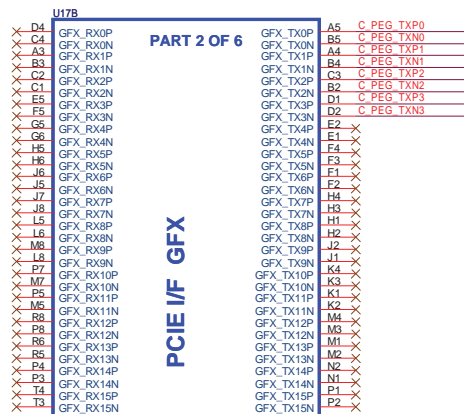
signals	Discrete
HT_TXCALP	R430 1.21k ohm 1%
HT_TXCALN	
HT_RXCALP	R434 1.21k ohm 1%
HT_RXCALN	

RES CHIP 1.21K 1/16W +-1%(0402)
P/N : CS21212FB18



This block is for UMA only . Discrete can remove all component





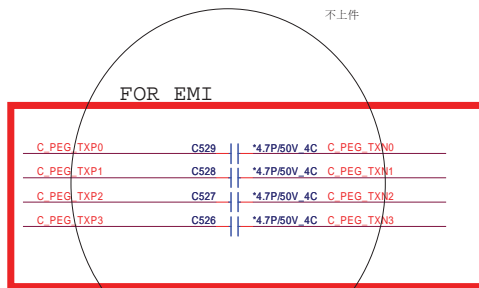
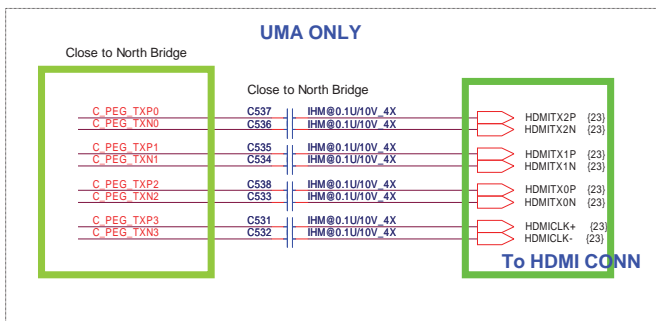
RS880 Display Port Support (muxed on GFX)

DP0	GFX_TX0, TX1, TX2 and TX3 AUX0 and HPD0
DP1	GFX_TX4, TX5, TX6 and TX7 AUX1 and HPD1

NOTE :
RS880MC no support Graphic / HDMI / DP

BOM Option Table

Reference	Description
IHM@	INT VGA & HDMI
IDP@	INT VGA & DP
EV@	EXT VGA



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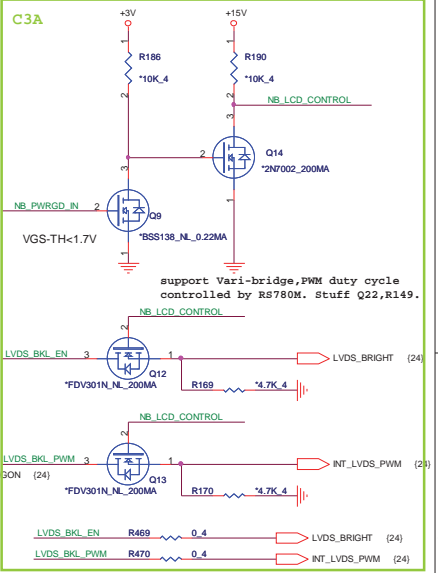
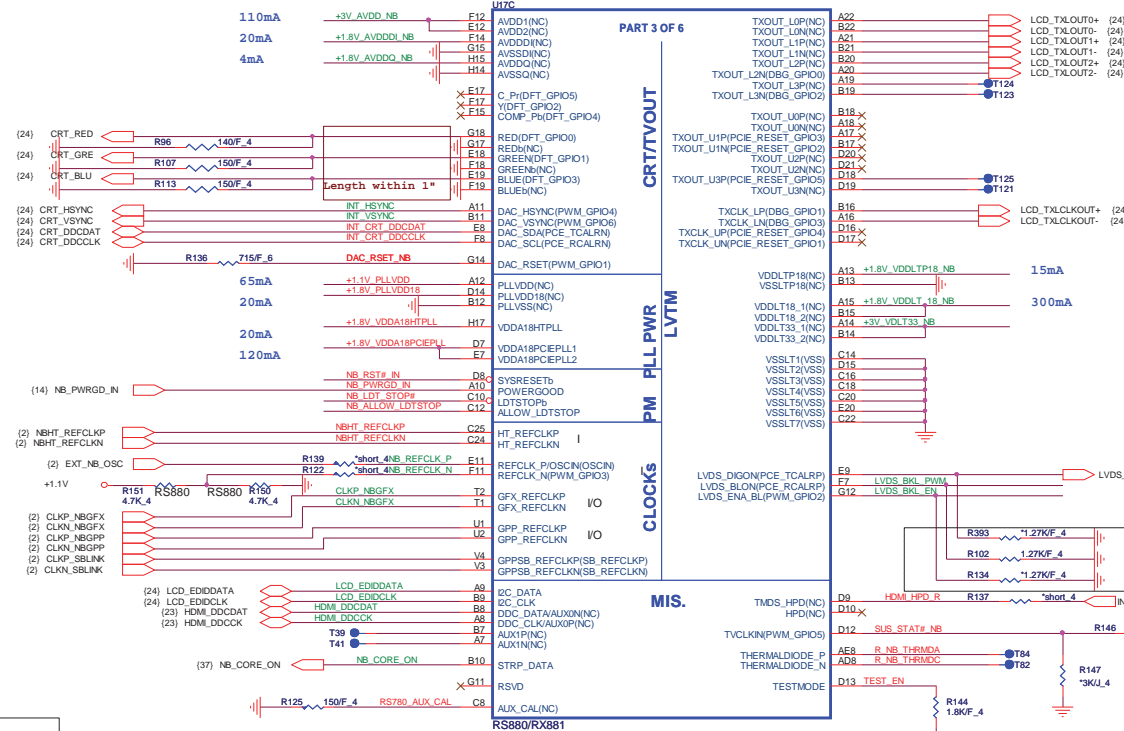
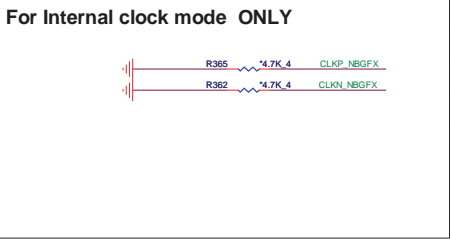
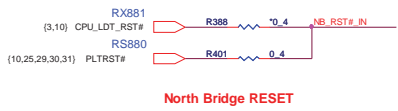
Quanta Computer Inc.
PROJECT : TE3

Size	Document Number	Rev
	RS880M-PCIE I/F 2/4	1A
Date: Tuesday, March 02, 2010	Sheet	7 of 43

BOM Option Table

Reference	Description
IV@	INT VGA
EV@	EXT VGA
IHM@	INT VGA & HDMI
IDP@	INT VGA & DP
ICRT@	INT VGA & CRT

[CLG]
[CLG]-IV@
[CRT]-ICRT@



RX881

RS880 Mobile Platforms: Two-level voltage scaling solution used; Pulled down through a 2-k Ω 5% resistor and connected to the Northbridge core power supply (circuit.don't need to do something for the RX881)

RS880

Debug Bus

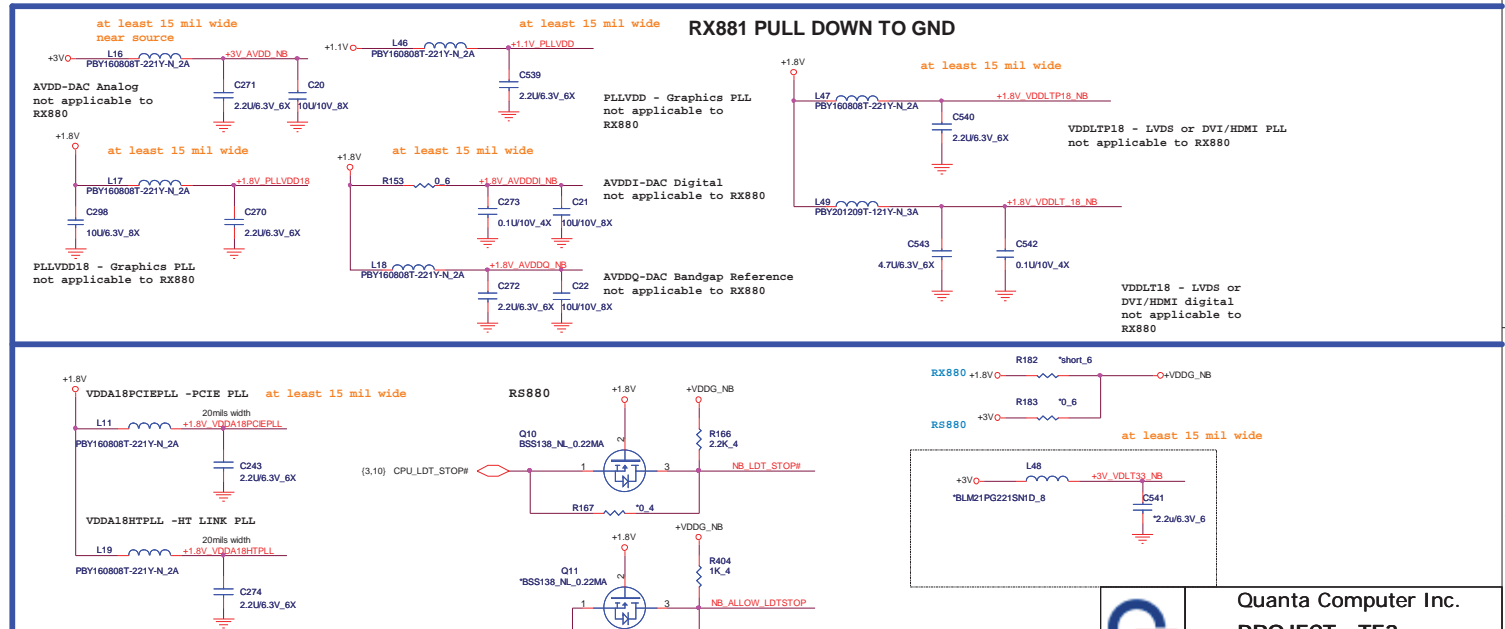
0-->Enable
1-->Disable

Sideport Enable

0-->Enable
1-->Disable

EEPROM Strap

0-->Enable
1-->Disable

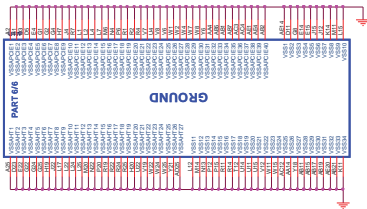


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Quanta Computer Inc.
PROJECT : TE3

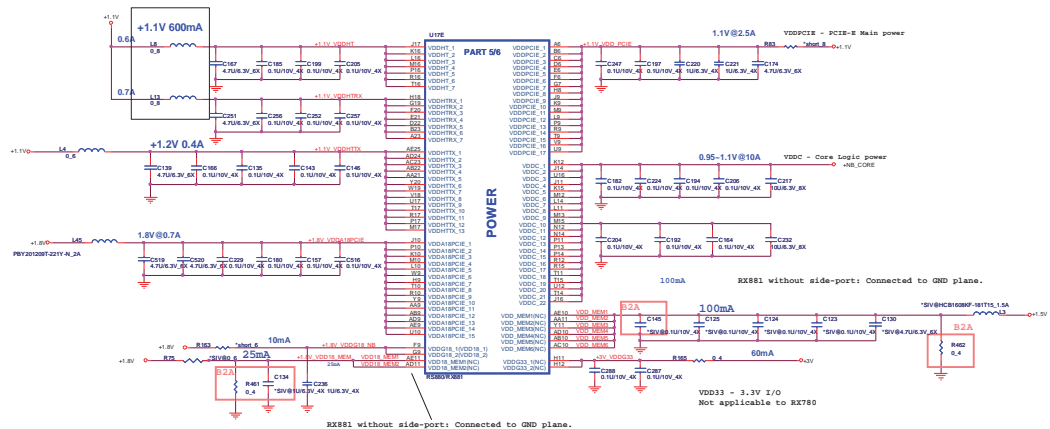
Size	Document Number	Rev
	RS880-SYSTEM I/F 3/4	1A

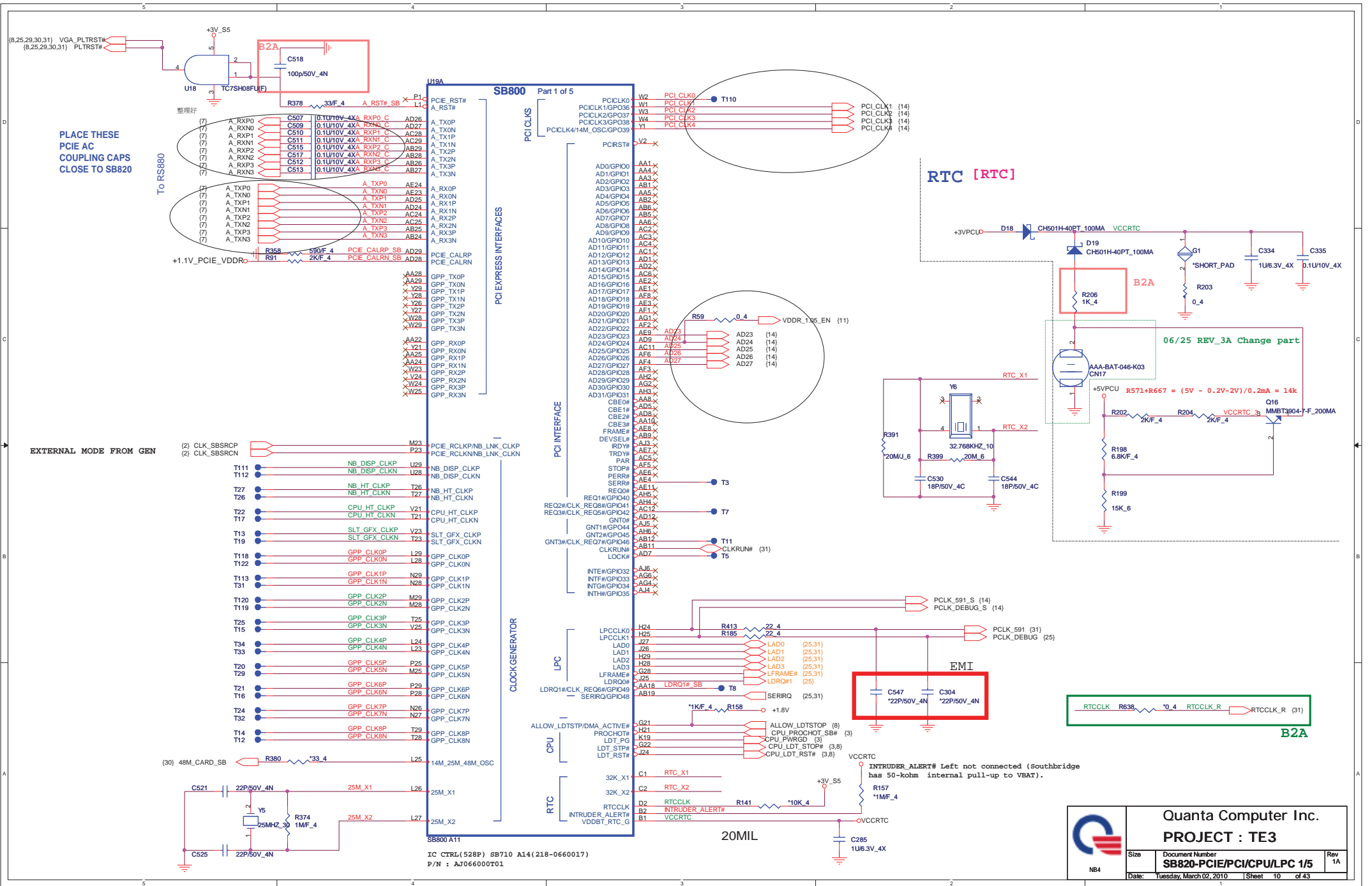
Date: Tuesday, March 02, 2010 Sheet 8 of 43



RX881/RS880 POWER DIFFERENCE TABLE

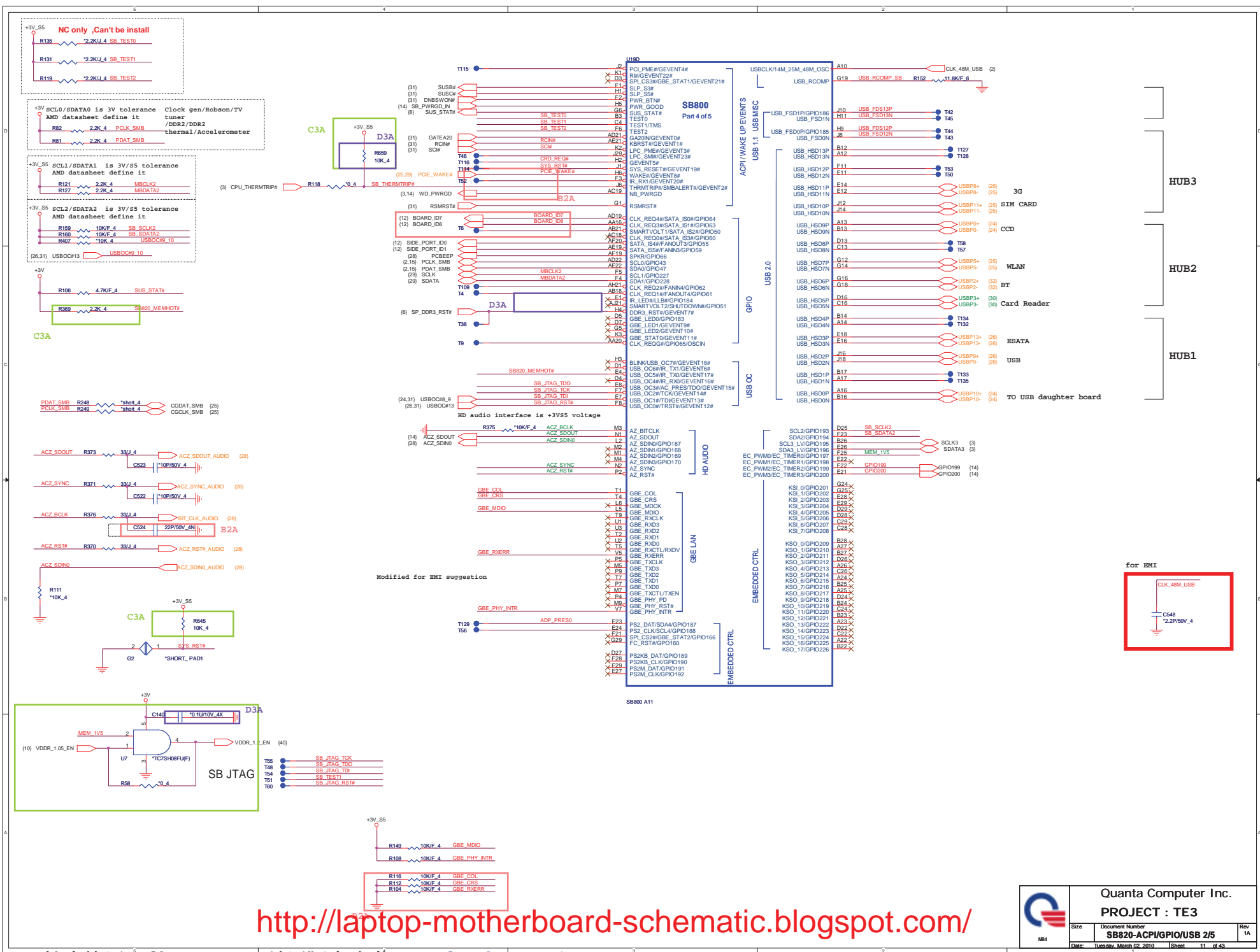
PIN NAME	RS880	PIN NAME	RS880
VDDHT	+1.1V	IOPLVDD	+1.1V
VDDHTEX	+1.1V	AVDD	+3.3V
VDDHTEX	+1.2V	AVDD0	+1.8V
VDDA0PCIE	+1.8V	AVDD0	+1.8V
VDD0TB	+1.8V	FLVDD	+1.1V
VDD0T_NEW	+1.8V	FLVDD0	+1.8V
VDDPCIE	+1.1V	VDDA0PCIEPCIE	+1.8V
VDDC	+1.1V	VDDA0T0TFL	+1.8V
VDD_MEM	1.5V	VDDPCIF1	+1.8V
VDD0S3	+3.3V	VDD0T18	+1.8V
IOPLVDDTB	+1.8V	VDD0T3	N/A





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		Quanta Computer Inc. PROJECT : TE3	
		Size SB820-PCIE/PCI/CPULPC 1/5	Rev 1A
Date: Tuesday, March 02, 2010	Sheet 10 of 43		



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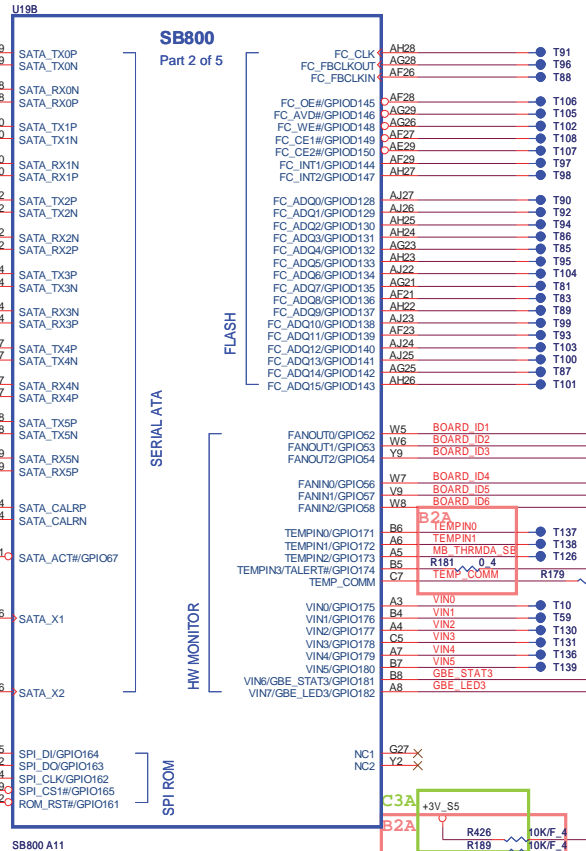
	Quanta Computer Inc. PROJECT : TE3	
	Size: SB820-ACPI/GPIO/USB 2/5	Rev: 1A
Date: Tuesday, March 02, 2010	I: Sheet	of 43

SATA PORT 0,1,2,3
can support AHCI
mode

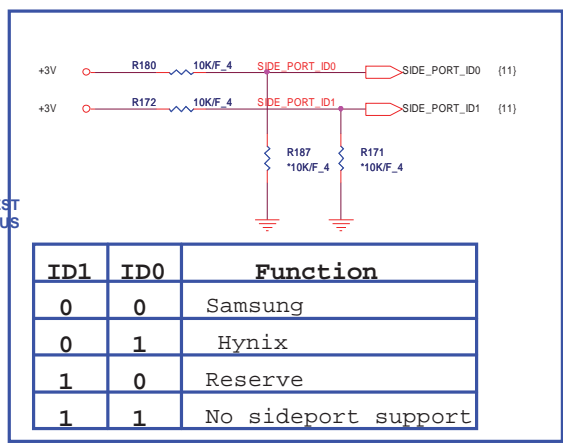
SATA HDD
(27) SATA_TXP0
(27) SATA_TXN0
(27) SATA_RXN0
(27) SATA_RXP0

E-SATA
(26) SATA_RXN2
(26) SATA_RXP2

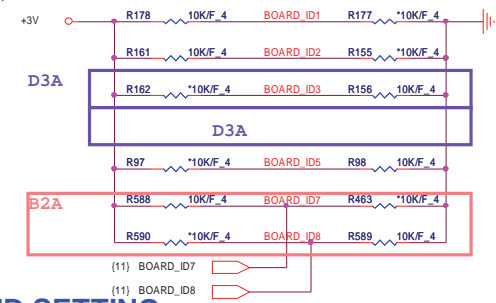
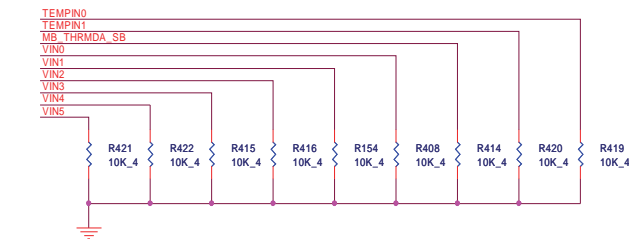
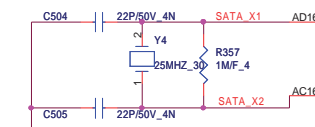
SATA ODD
(27) SATA_TXP1
(27) SATA_TXN1
(27) SATA_RXN1
(27) SATA_RXP1



IF THERE IS NO ID# TEST POINTS FOR DEBUG BUS IS MANDATORY



PLACE SATA_CAL RES VERY CLOSE TO BALL OF SB820



BOARD ID SETTING

Board ID	ID1	ID2	ID3	ID4	ID5	ID6	ID7	ID8
UMA SKU VGA SKU	H L							
W/ MDC W/O MDC		H L						
W/ HDMI W/O HDMI			H L					
W/O 3G W/ 3G				H L				
15" 14"					H L			
W/O BT W/ BT						H L		
1.3W 1.7W							H L	
W/ CF W/O CF								H L

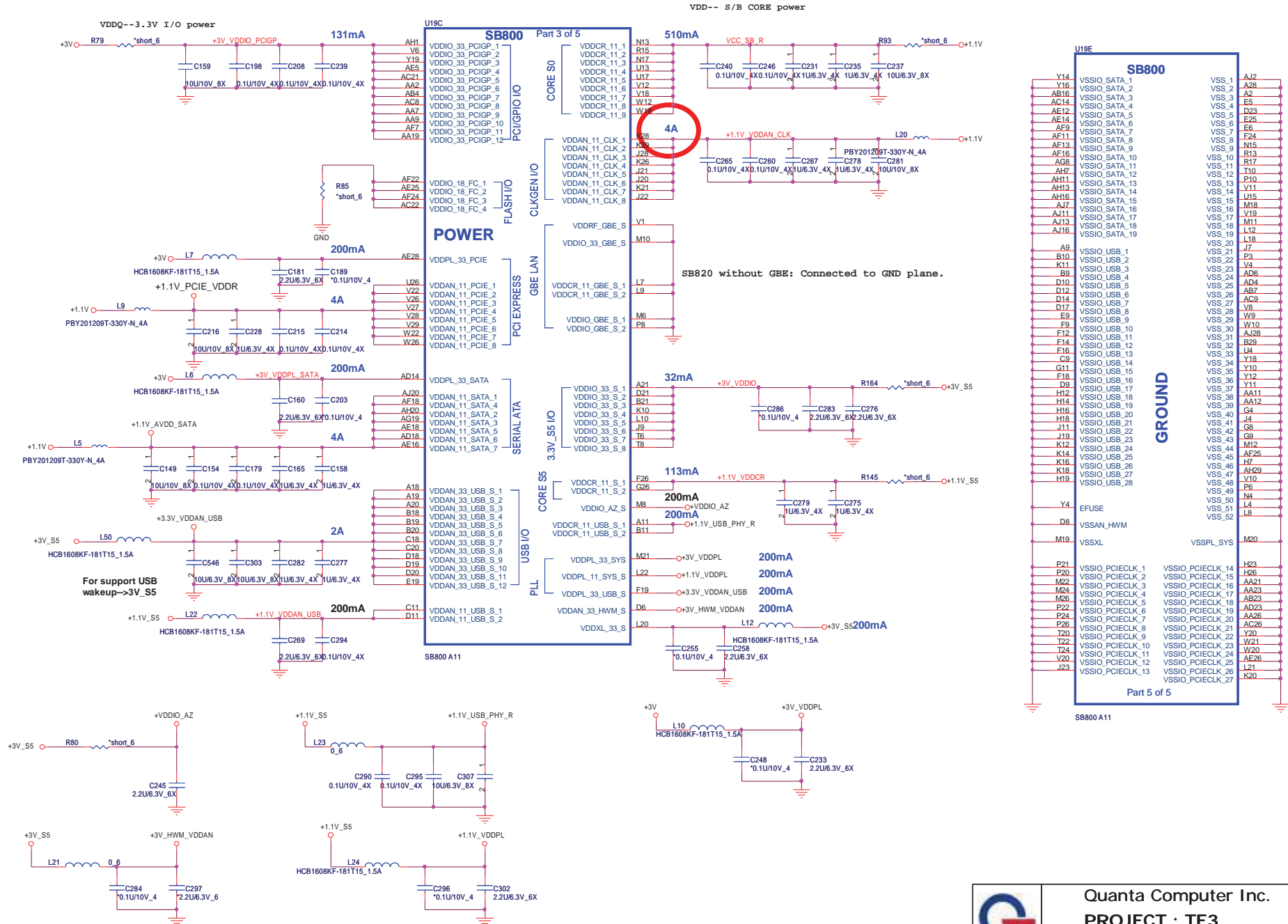
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Quanta Computer Inc.
PROJECT : TE3

Size: Document Number: **SB820-SATA/IDE/HWM/SPI 3/5** Rev: 1A

Date: Tuesday, March 02, 2010 Sheet 12 of 43

PLACE ALL THE DECOUPLING CAPS ON THIS SHEET CLOSE TO SB AS POSSIBLE.



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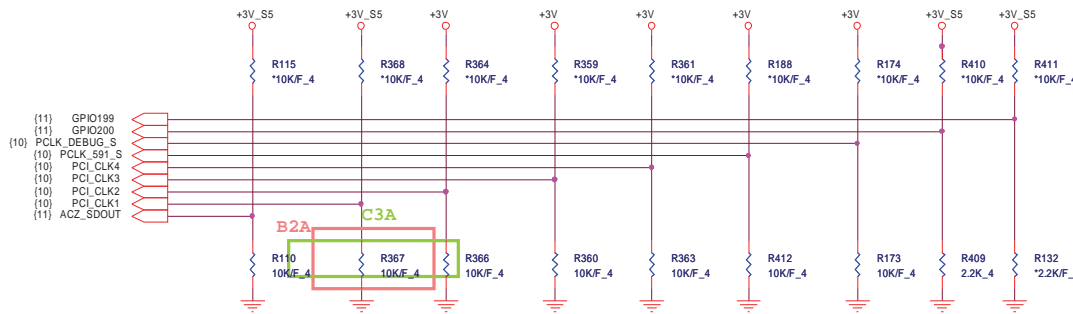


Quanta Computer Inc.		
PROJECT : TE3		
Size	Document Number	Rev
	SB820-PWR/DECOUPLING 4/5	1A
Date:	Tuesday, March 02, 2010	Sheet 13 of 43

REQUIRED STRAPS



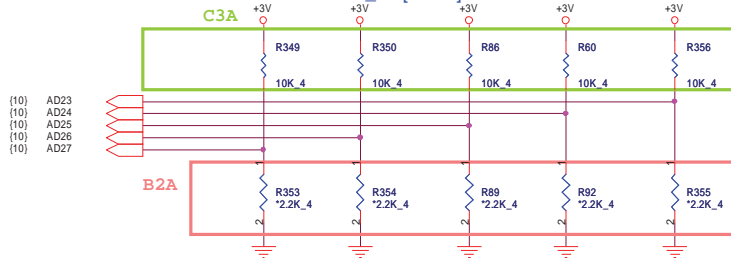
OVERLAP COMMON PADS WHERE POSSIBLE FOR DUAL-OP RESISTORS.



	AZ_SDOUT	PCI_CLK1	PCI_CLK2	PCI_CLK3	PCI_CLK4	LPC_CLK0	LPC_CLK1	GPIO200 (PWM3)	GPIO199 (PWM2)
PULL HIGH	LOW POWER MODE	ALLOW PCIE Gen2 DEFAULT	Watchdog Timer Enable	USE DEBUG STRAPS	non_Fusion CLOCK MODE DEFAULT	EC ENABLED	CLKGEN ENABLED DEFAULT	GPIO200 (PWM3) GPIO199 (PWM2)	H, H=Reserved H, L=SPI ROM
PULL LOW	PERFORMANCE MODE DEFAULT	FORCE PCIE Gen1	Watchdog Timer Disable DEFAULT	IGNORE DEBUG STRAPS DEFAULT	Fusion CLOCK MODE	EC DISABLED DEFAULT	CLKGEN DISABLED	L, H=LPC ROM L, L=FWH ROM	DEFAULT

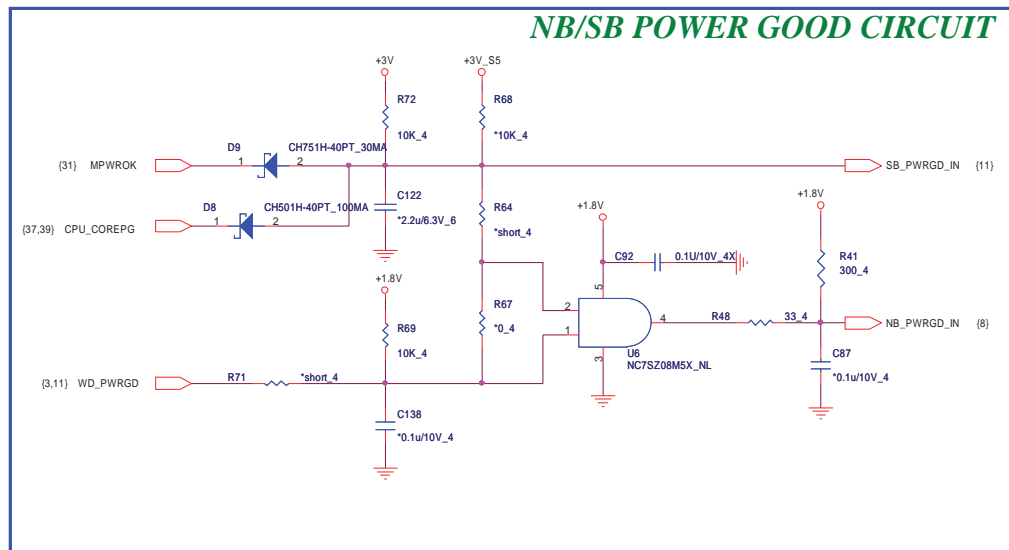
DEBUG STRAPS

SB800 HAS 15K INTERNAL PU FOR PCI_AD[27:23]



	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE PCI PLL DEFAULT	DISABLE ILA AUTORUN DEFAULT	USE FC PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	DISABLE PCI MEM BOOT DEFAULT
PULL LOW	BYPASS PCI PLL	ENABLE ILA AUTORUN	BYPASS FC PLL	USE EEPROM PCIE STRAPS	ENABLE PCI MEM BOOT

NB/SB POWER GOOD CIRCUIT



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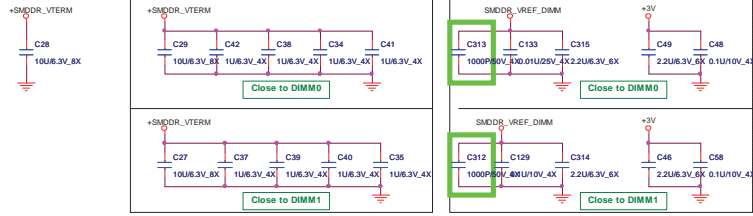


Quanta Computer Inc.
PROJECT : TE3

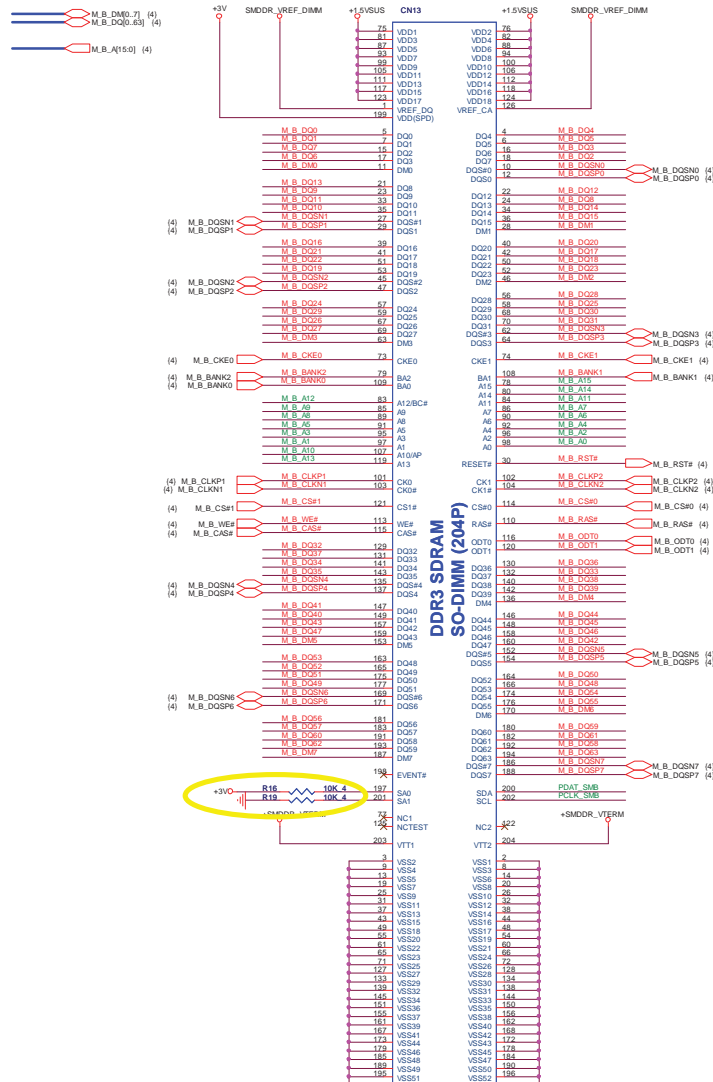
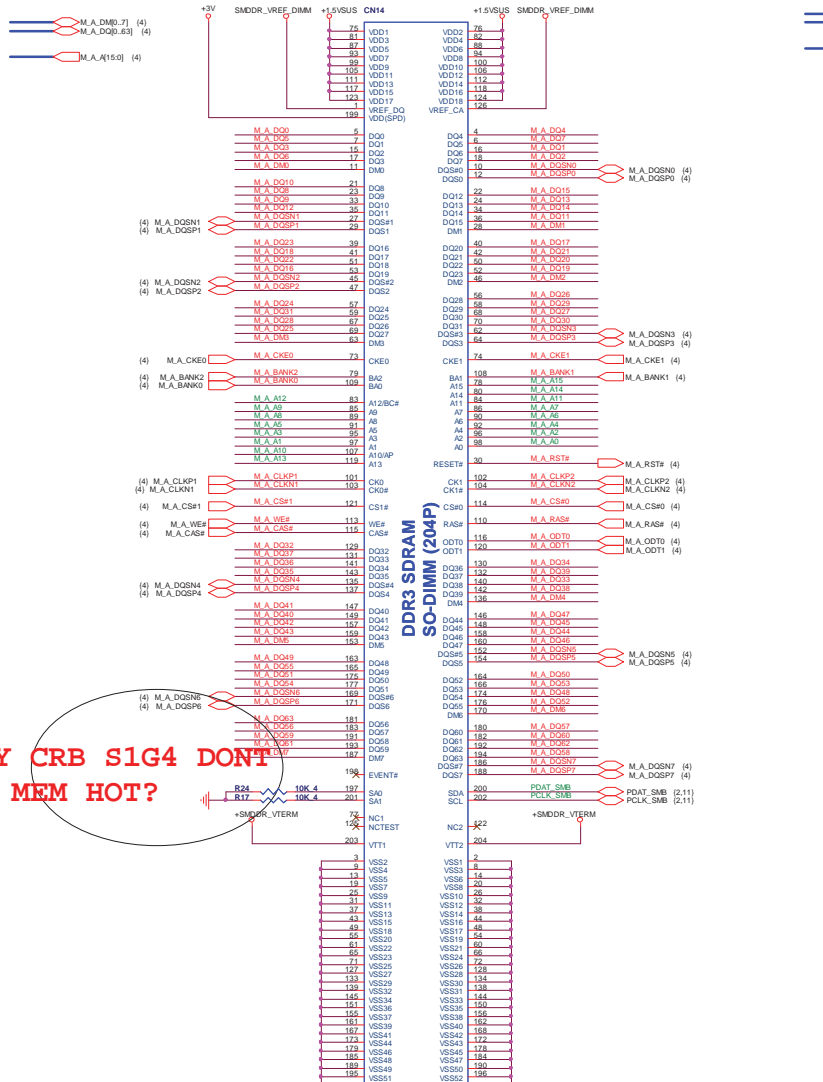
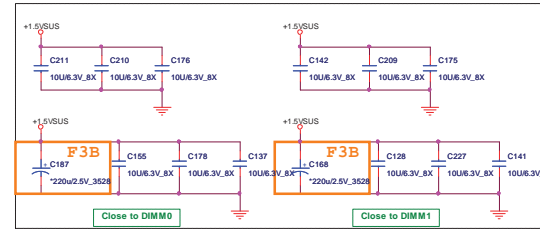
Size	Document Number	Rev
	SB820-STRAPS 5/5	1A
Date:	Tuesday, March 02, 2010	Sheet 14 of 43

DDRIII

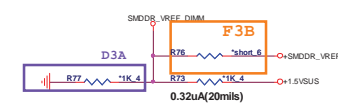
TERMINATOR DECOUPLING CAPACITOR



9.12A(VCC plane from source)




CHECK BY CRB S1G4 DONT SUPPORT MEM HOT?



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Quanta Computer Inc.	
PROJECT : TE3	
Site: N84	Document Number: DDR3 DIMM TERMINATOR
Rev: 1A	Date: 1/24/2010 11:02:00 AM Sheet: 16 of 43




 NB4	Quanta Computer Inc.		
	PROJECT : TE3		
	Size Custom	Document Number M93-S3_PCIE_Interface	Rev 1A
Date: Tuesday, March 02, 2010		Sheet 16 of 43	

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 NB4	Quanta Computer Inc.	
	PROJECT : TE3	
Size Custom	Document Number M93-S3_GND / LVDS/ Straps	Rev 1A
Date: Tuesday, March 02, 2010	Sheet 18	of 43



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 NB4	Quanta Computer Inc.	
	PROJECT : TE3	
Size Custom	Document Number M93-S3_Power_and_NC	Rev 1A
Date: Tuesday, March 02, 2010	Sheet 19	of 43



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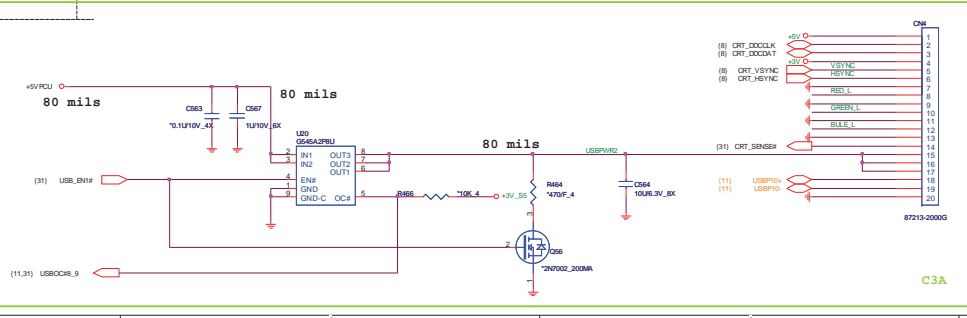
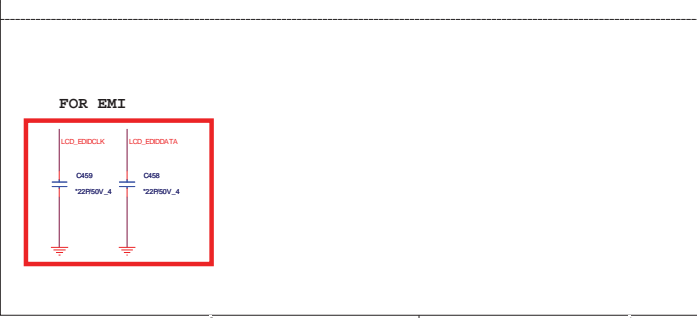
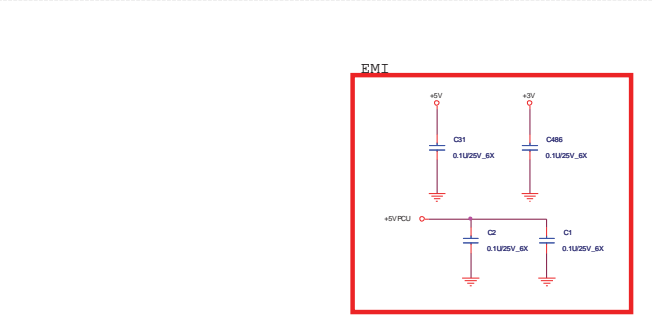
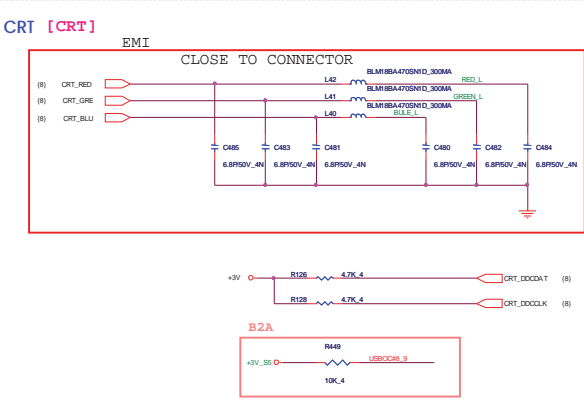
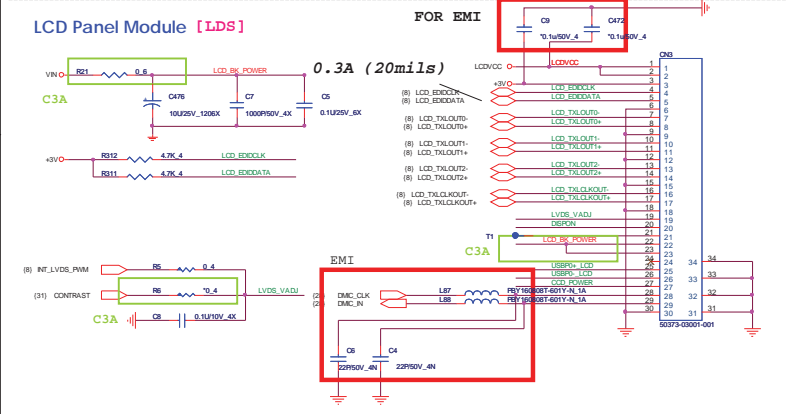
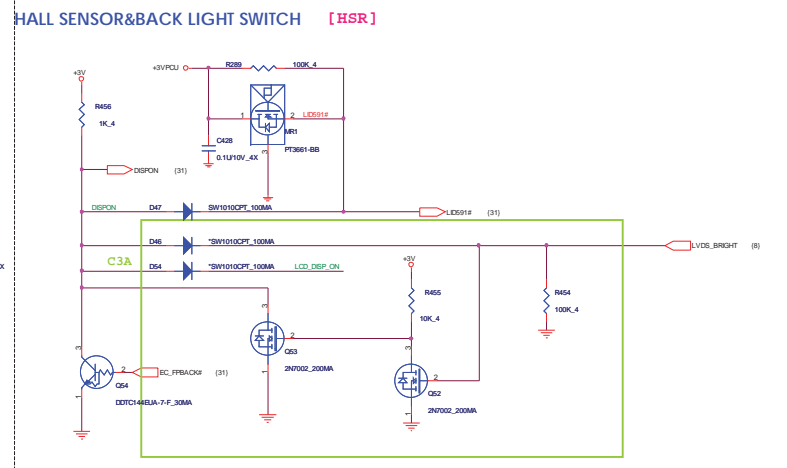
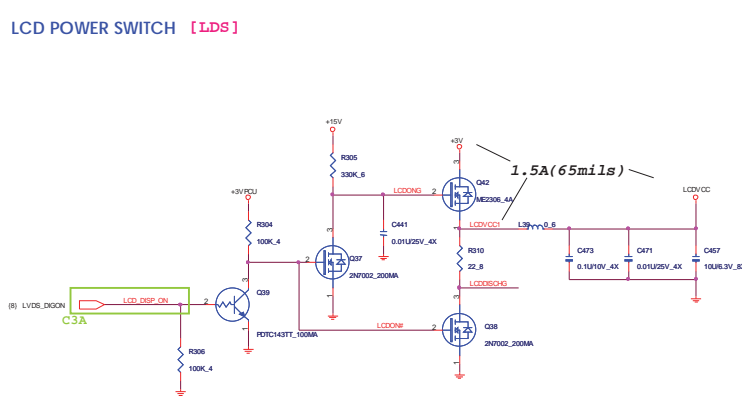
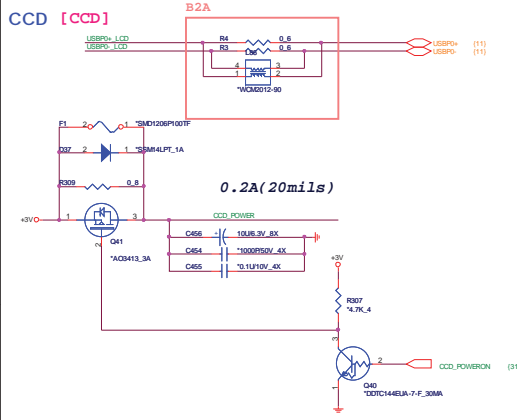


Quanta Computer Inc.
PROJECT : TE3

Size Custom	Document Number M93-S3_Interface	Rev 1A
Date: Tuesday, March 02, 2010		Sheet 21 of 43

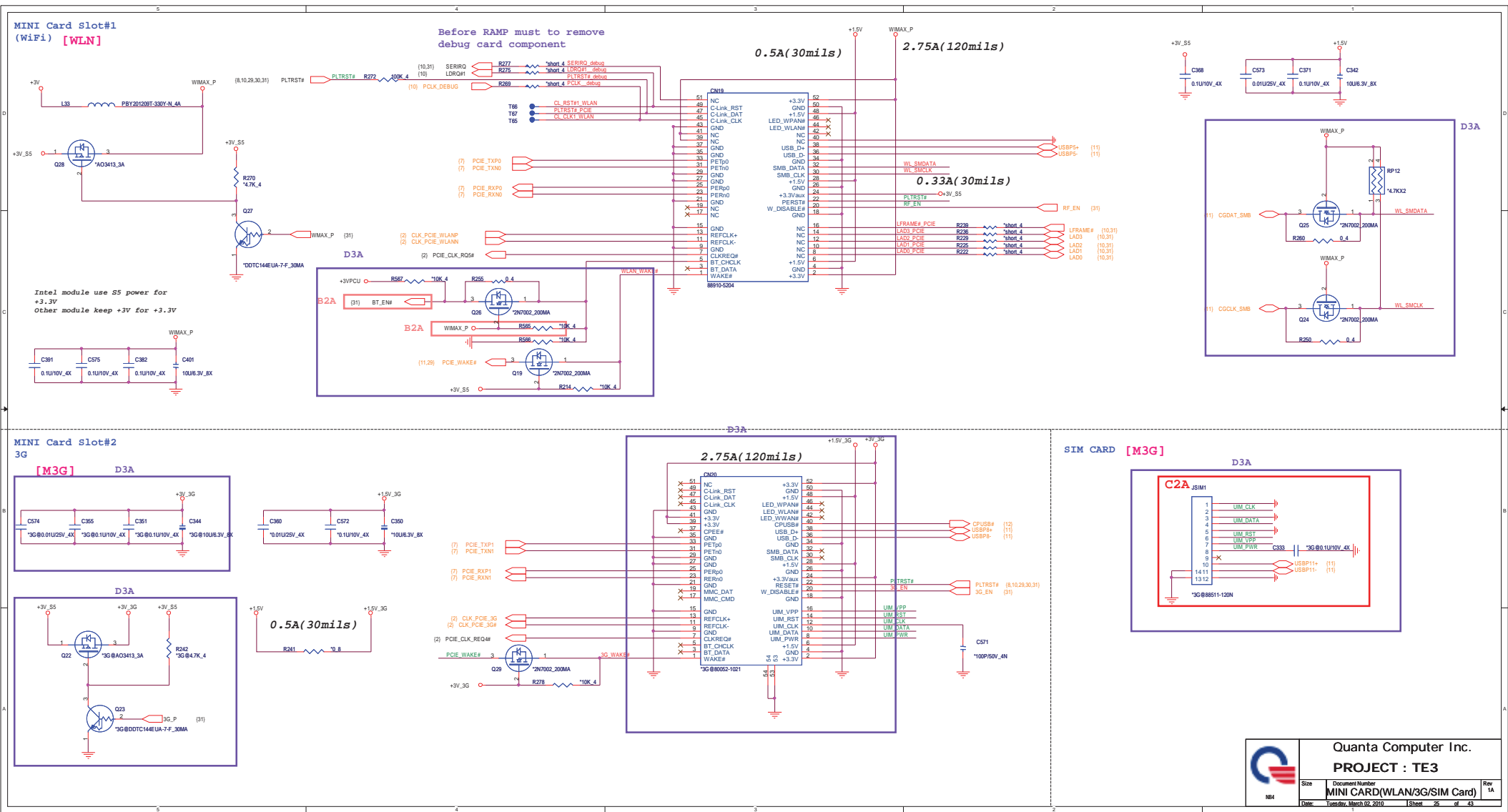
<http://laptop-motherboard-schematic.blogspot.com/>

	Quanta Computer Inc.	
	PROJECT : TE3	
	Size Custom	Document Number M93-S3 VRAM_A0,A1
NB4	Date: Tuesday, March 02, 2010	Rev 1A
	Sheet 22 of 48	



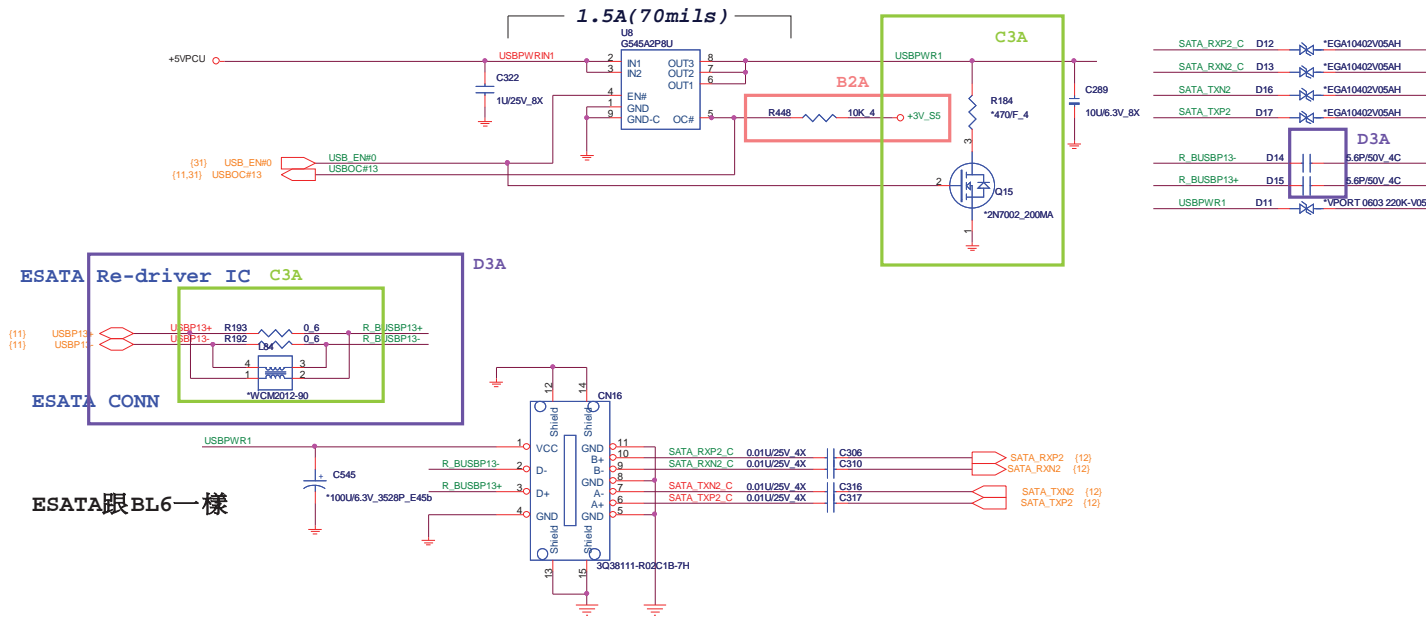
		Quanta Computer Inc. PROJECT : TE3	
Doc No	Doc Rev	Doc Date	Doc Desc
N84	1A	Tuesday, March 03, 2010	87219-2000G

<http://laptop-motherboard-schematic.blogspot.com/>

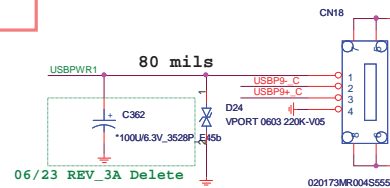
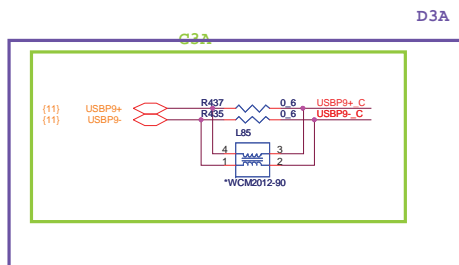
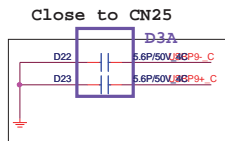
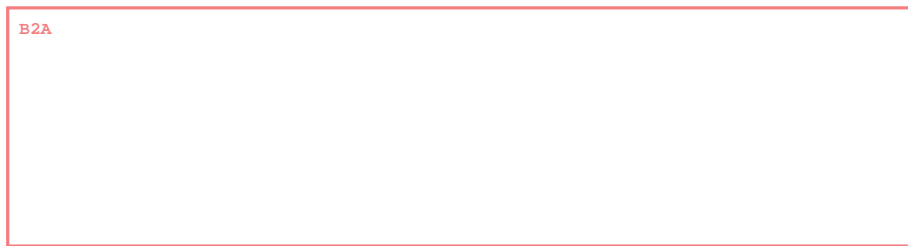


<http://laptop-motherboard-schematic.blogspot.com/>


E-SATA [ESA]



USB MB SIDE



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		Quanta Computer Inc.	
		PROJECT : TE3	
Site	Document Number	Rev	
MB	CPW/ESATA/USB+Audio/LED	1A	
Date	Thursday, March 04, 2010	Sheet	26 of 43

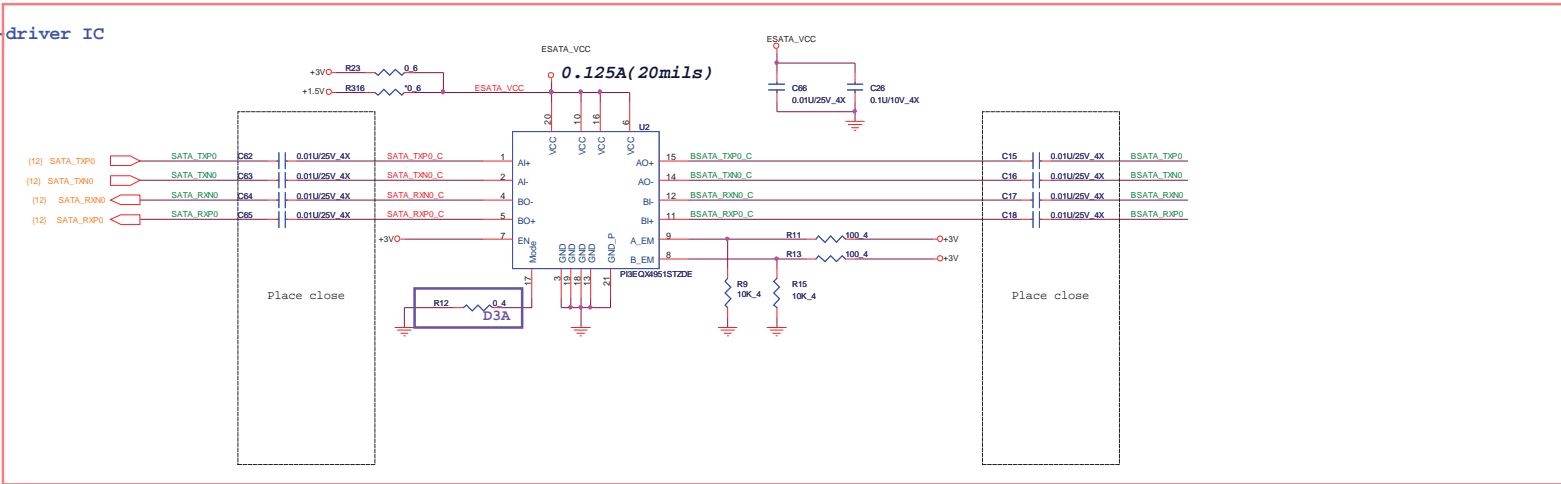
SATA ODD

[ODD]



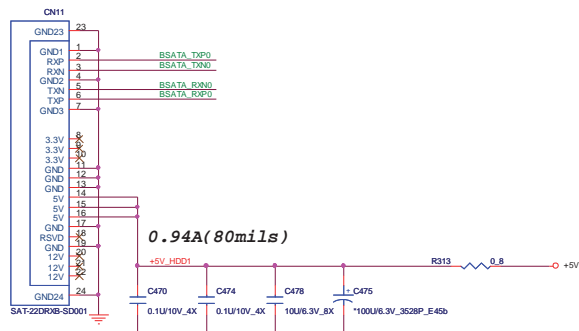
SATA HDD Re-driver IC

B2A

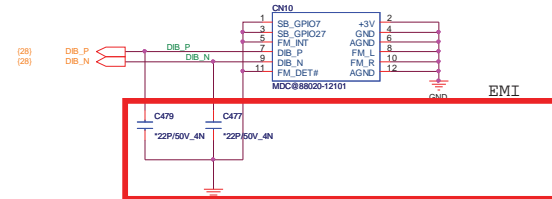


SATA HDD

[HDD]



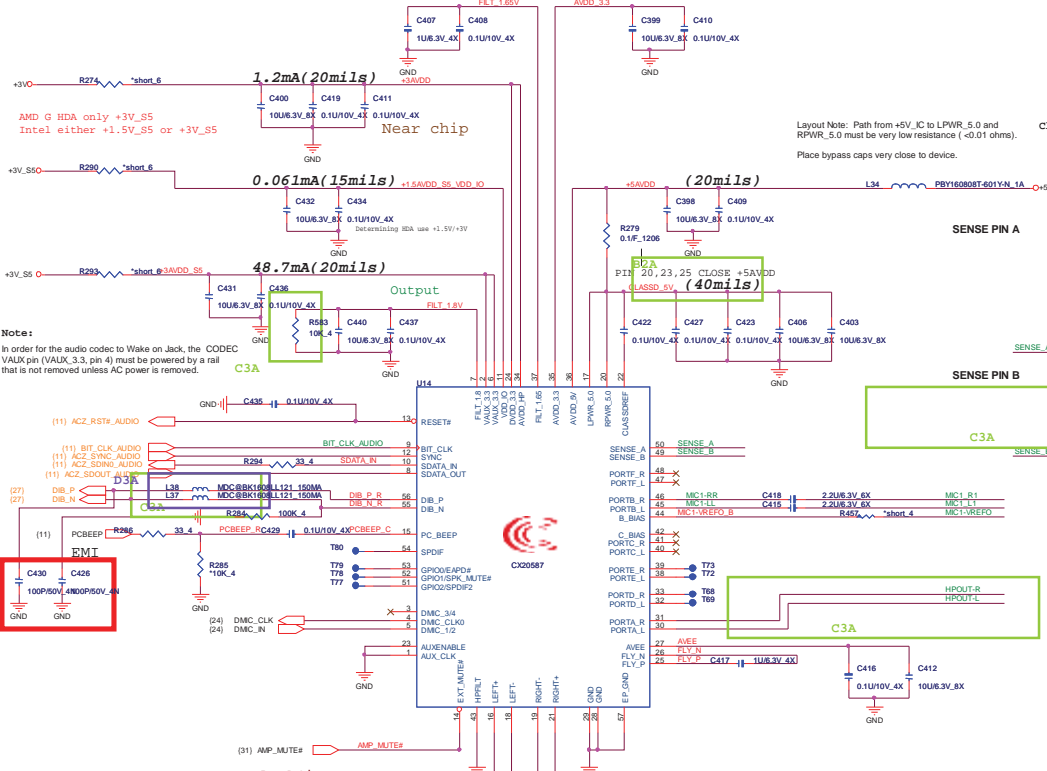
MDC



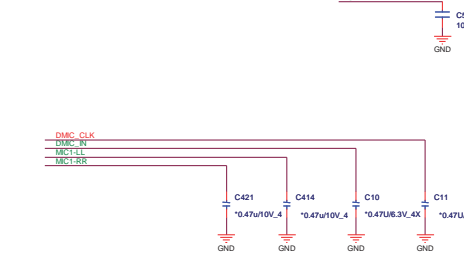
<http://laptop-motherboard-schematic.blogspot.com/>

Quanta Computer Inc.		
PROJECT : TE3		
Size	Document Number	Rev
NB4	HDD/ODD	1A
Date: Tuesday, March 02, 2010 Sheet: 27 of 43		

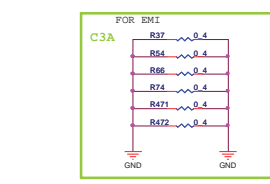
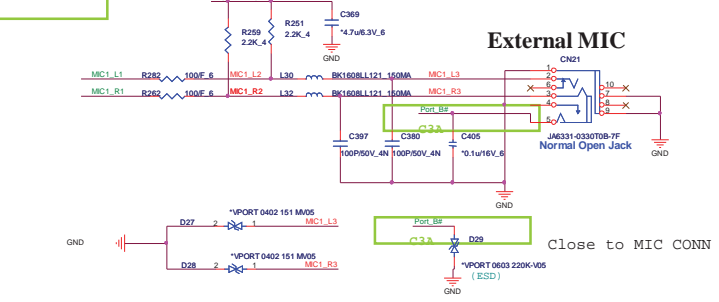
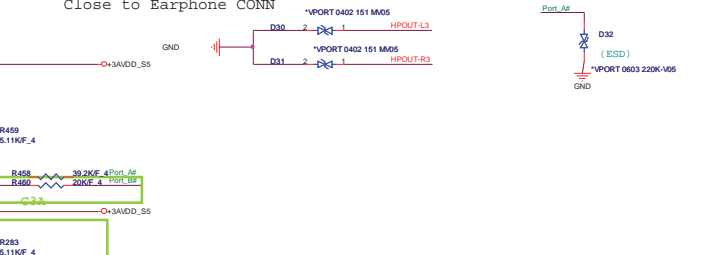
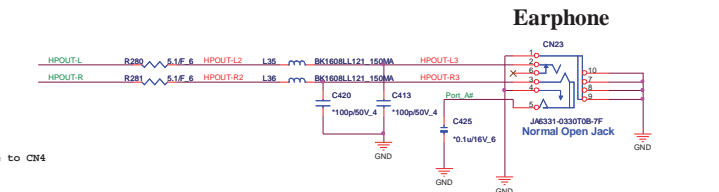
Codec (CX20583) [ADO]



SPDIF/BEEPAIN is an input used to set the PC BEEP gain while the device is in reset
 Default gain is -46 dB without populating the 10-kohm pull-up resistor.



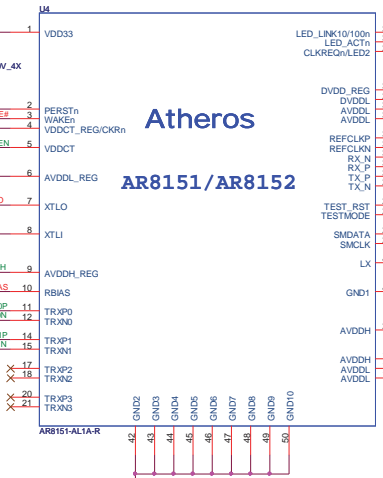
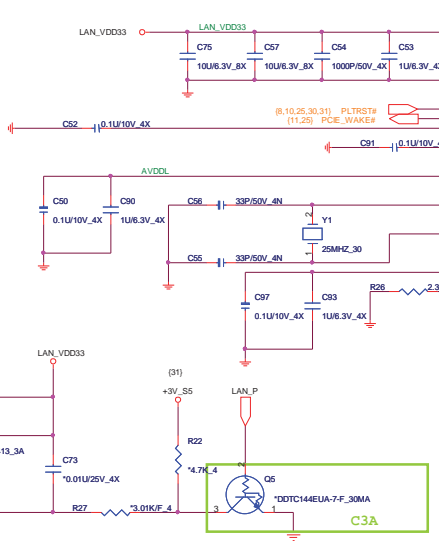
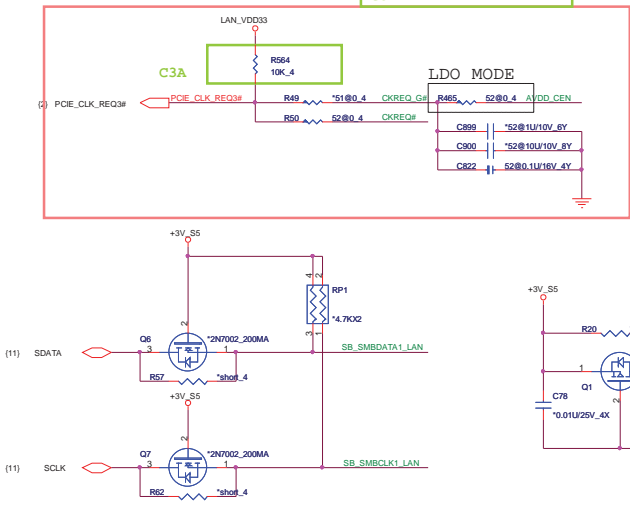
200910251600 remove MIC2_INT_L



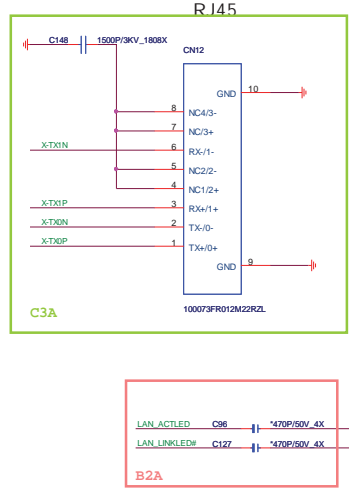
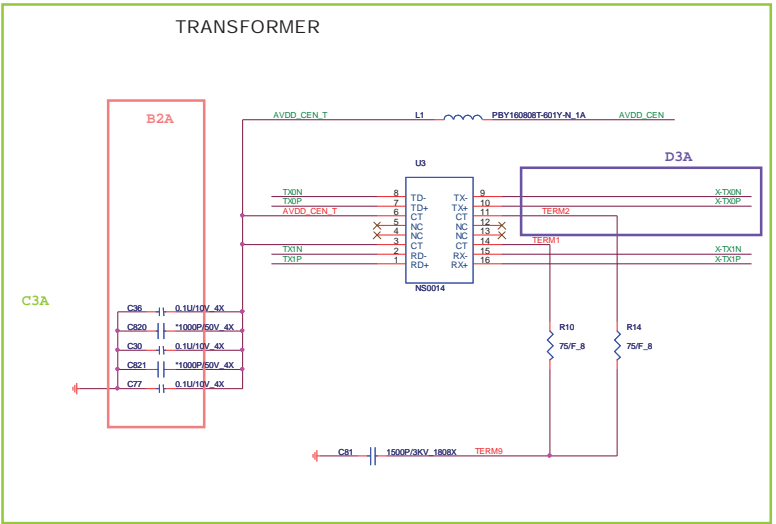
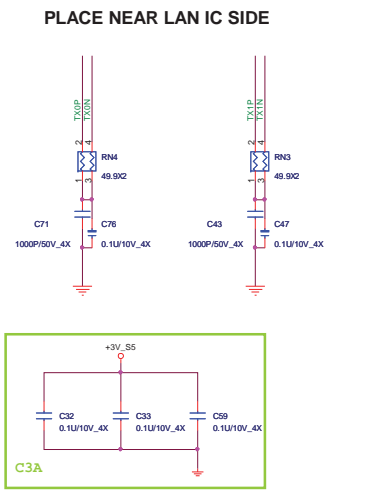
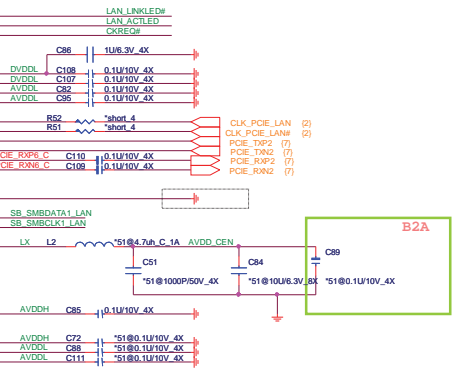
Quanta Computer Inc. PROJECT : TE3		
Size	Document Number	Rev
	Codec (CX20583)	1A
Date: Wednesday, March 03, 2010 1:58pm	Sheet	28 of 43

Atheros Lan

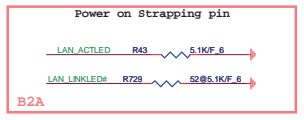
51_52@:
GIGA = NC
10/100 = 0 ohm CS00002JB3E



GIGA:AR8151-AL1A-R = AL008151001
10/100:AR8152-AL1A-R = AL008152004



LED0 = LAN_ACTLED	1	Over-clocking enable (default = 1)
	0	Over-clocking disable
LED1 = LAN_LINKLED#	1	SWR switch-mode regulator select Giga LAN pull High (default = 1)
	0	LDO linear regulator select 10/100 LAN pull Low
CKREQ# or CKREQ_G#	1	Normal function
	0	ATE test mode

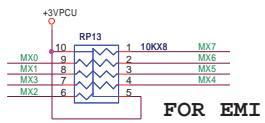


Quanta Computer Inc.
PROJECT : TE3

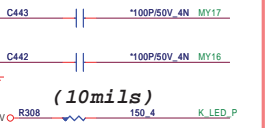
Size: Document Number: Atheros Lan Rev 1A
Date: Tuesday, March 02, 2010 Sheet 20 of 43

<http://laptop-motherboard-schematic.blogspot.com/>

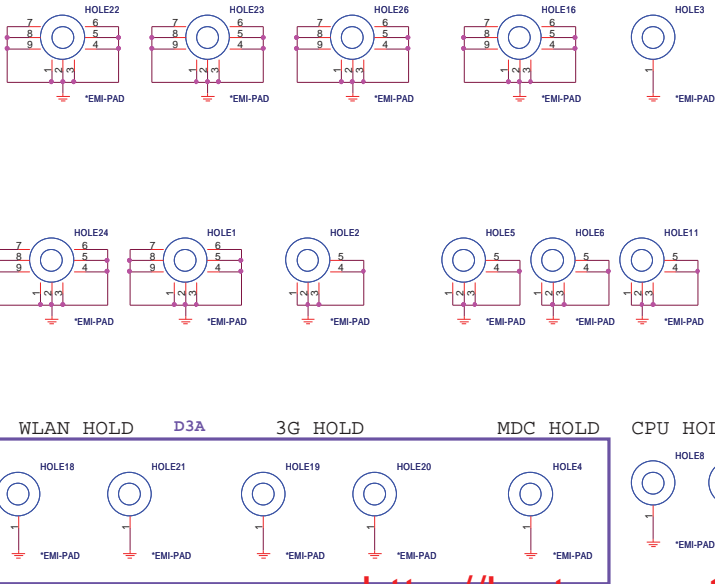
INT Keyboard [KBC]



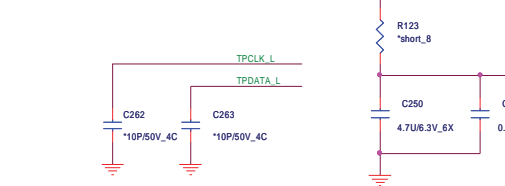
C456	*220P/50V_4N	MX7
C450	*220P/50V_4N	MX2
C467	*220P/50V_4N	MX3
C451	*220P/50V_4N	MX4
C468	*220P/50V_4N	MX0
C452	*220P/50V_4N	MX5
C469	*220P/50V_4N	MX6
C453	*220P/50V_4N	MX1
C464	*220P/50V_4N	MY7
C448	*220P/50V_4N	MY13
C465	*220P/50V_4N	MY12
C449	*220P/50V_4N	MY15
C462	*220P/50V_4N	MY3
C446	*220P/50V_4N	MY5
C463	*220P/50V_4N	MY14
C447	*220P/50V_4N	MY6
C460	*220P/50V_4N	MY2
C444	*220P/50V_4N	MY1
C461	*220P/50V_4N	MY0
C445	*220P/50V_4N	MY4



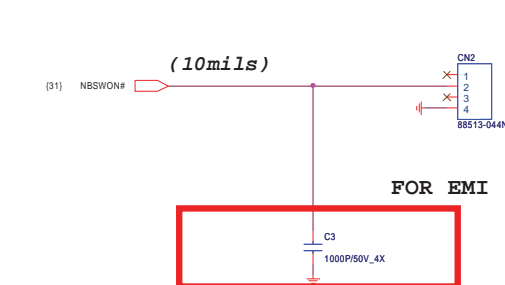
HOLE



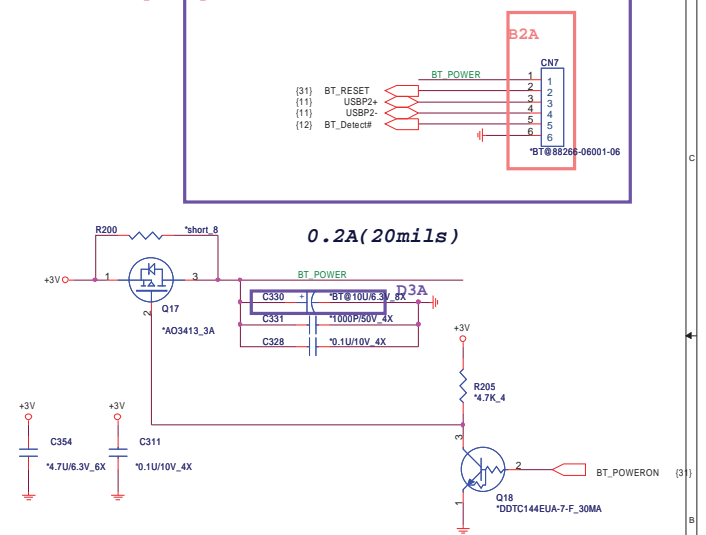
TP board [TPD]



Power board [PSW]



Bluetooth [BTM]



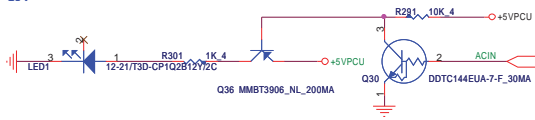
Quanta Computer Inc.
PROJECT : TE3

Size	Document Number	Rev
NBA	KB/TP&TP/PB/FL/LEB/MMB/B-CAS	
Date:	Tuesday, March 02, 2010	Sheet 32 of 43

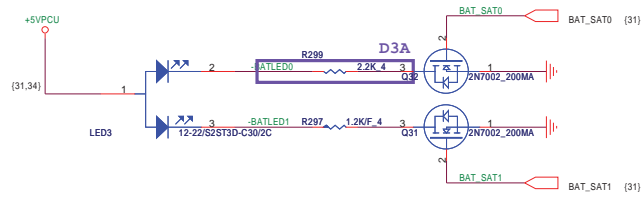
<http://laptop-motherboard-schematic.blogspot.com/>

LED [LED]

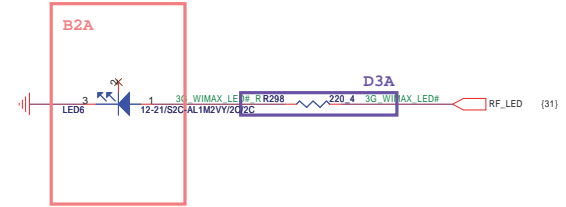
AC-IN



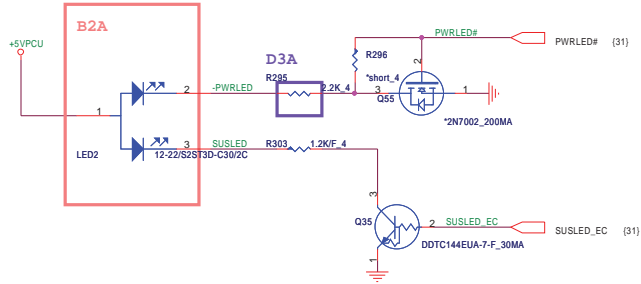
BATTERY



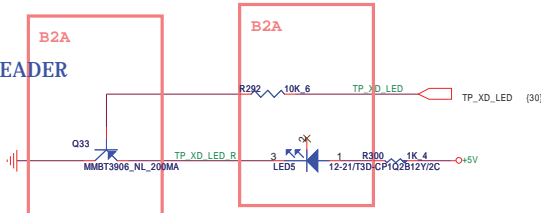
RF LED



POWER

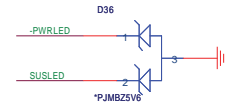


CARDREADER

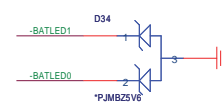


ESD Protect

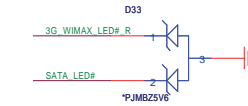
FOR POWER LED



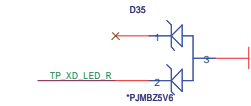
FOR BATTERY LED



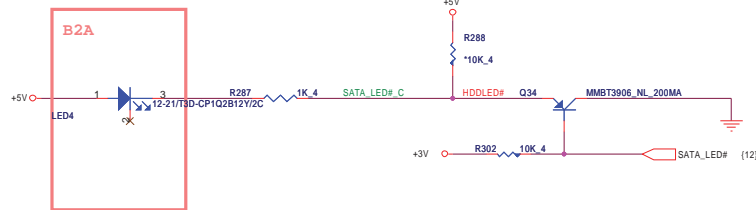
FOR HDD/W-LAN LED



FOR 3G/CARDREADER LED



HDD/ODD

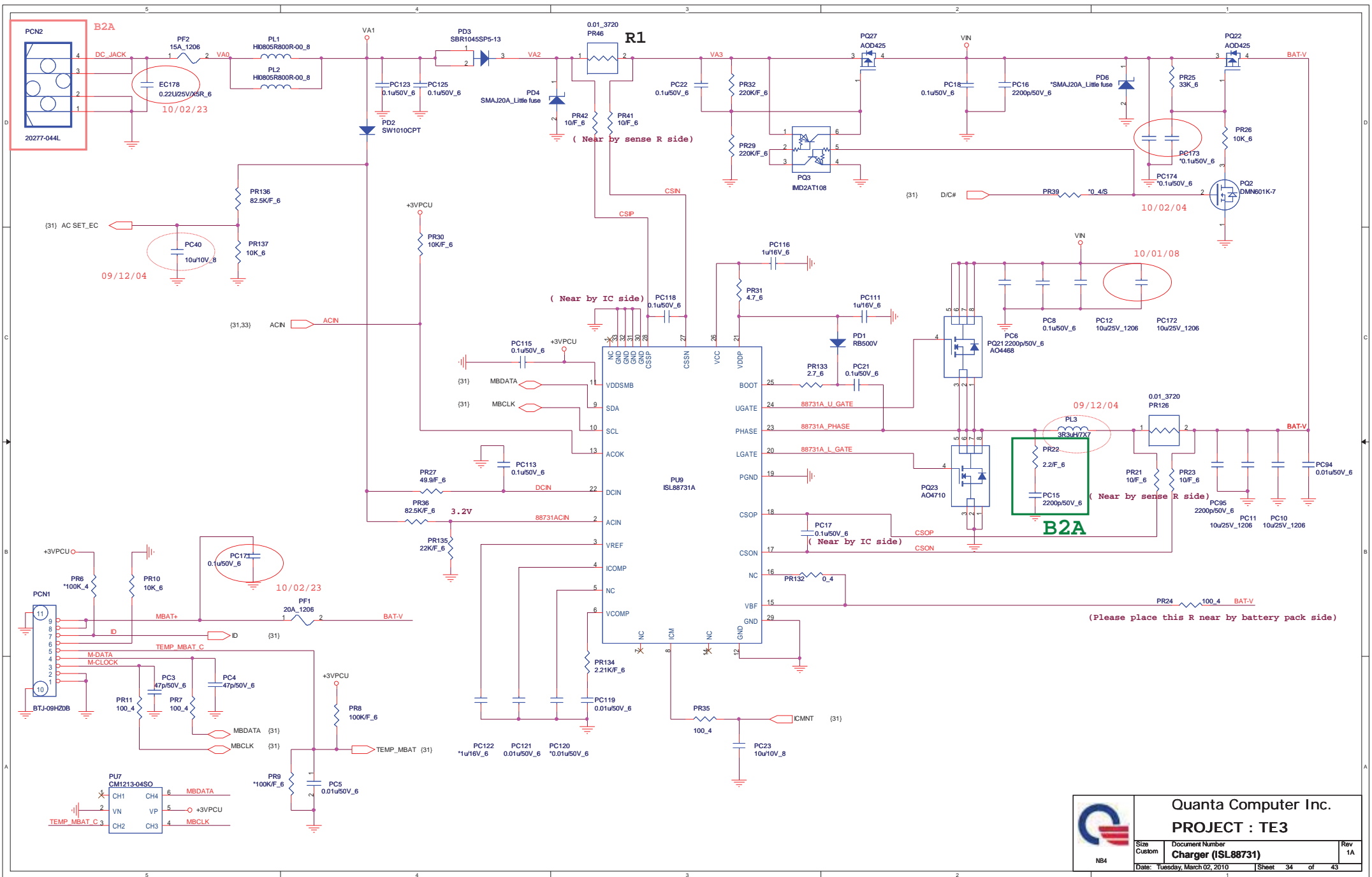



<http://laptop-motherboard-schematic.blogspot.com/>



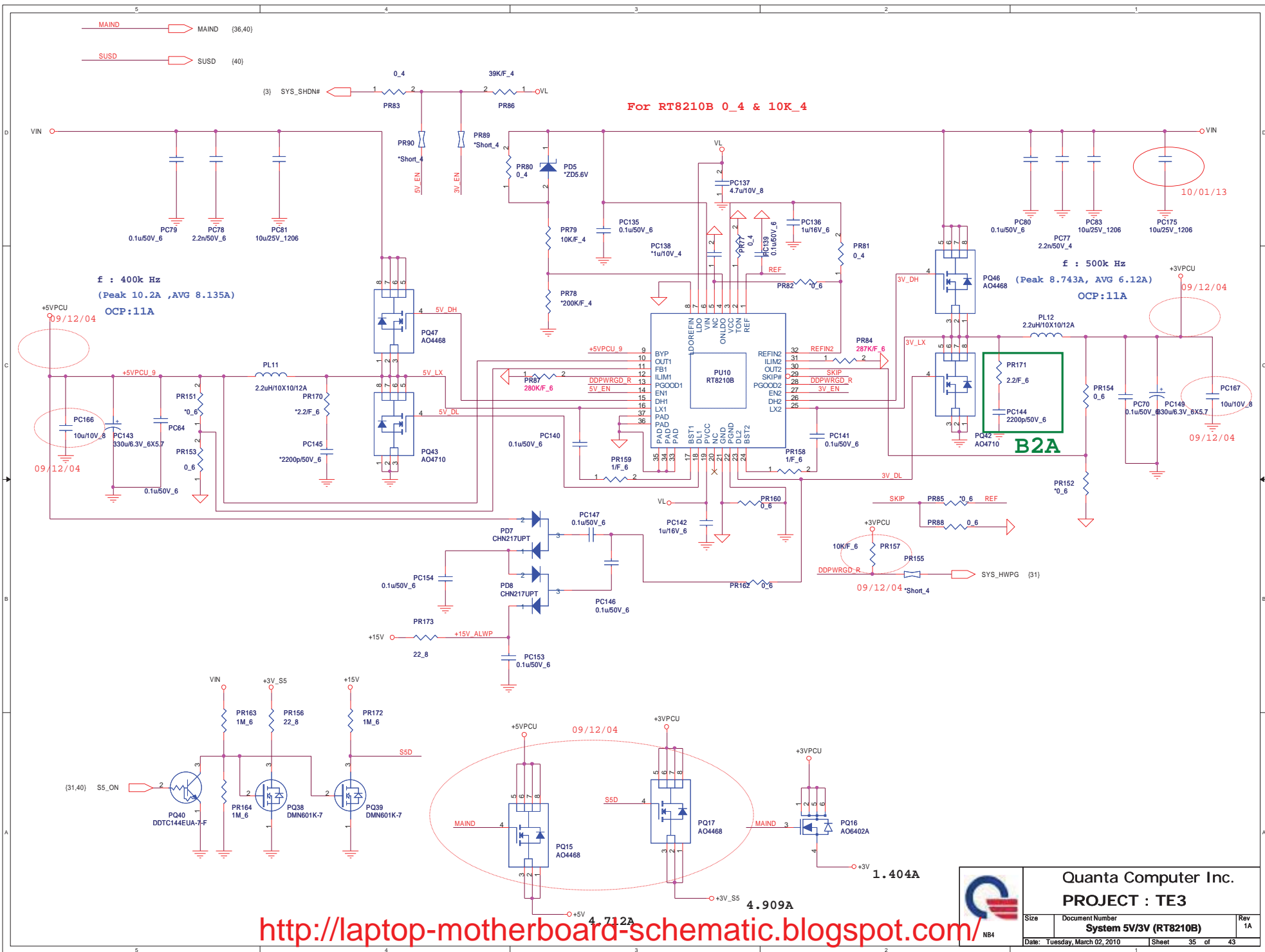
Quanta Computer Inc.
PROJECT : TE3

Size	Document Number	Rev
	LED/HOLE	1A
Date: Wednesday, March 03, 2010		Sheet 33 of 43



 NB4	Quanta Computer Inc.		
	PROJECT : TE3		
	Size Custom	Document Number Charger (ISL88731)	Rev 1A
Date: Tuesday, March 02, 2010		Sheet 34 of 43	

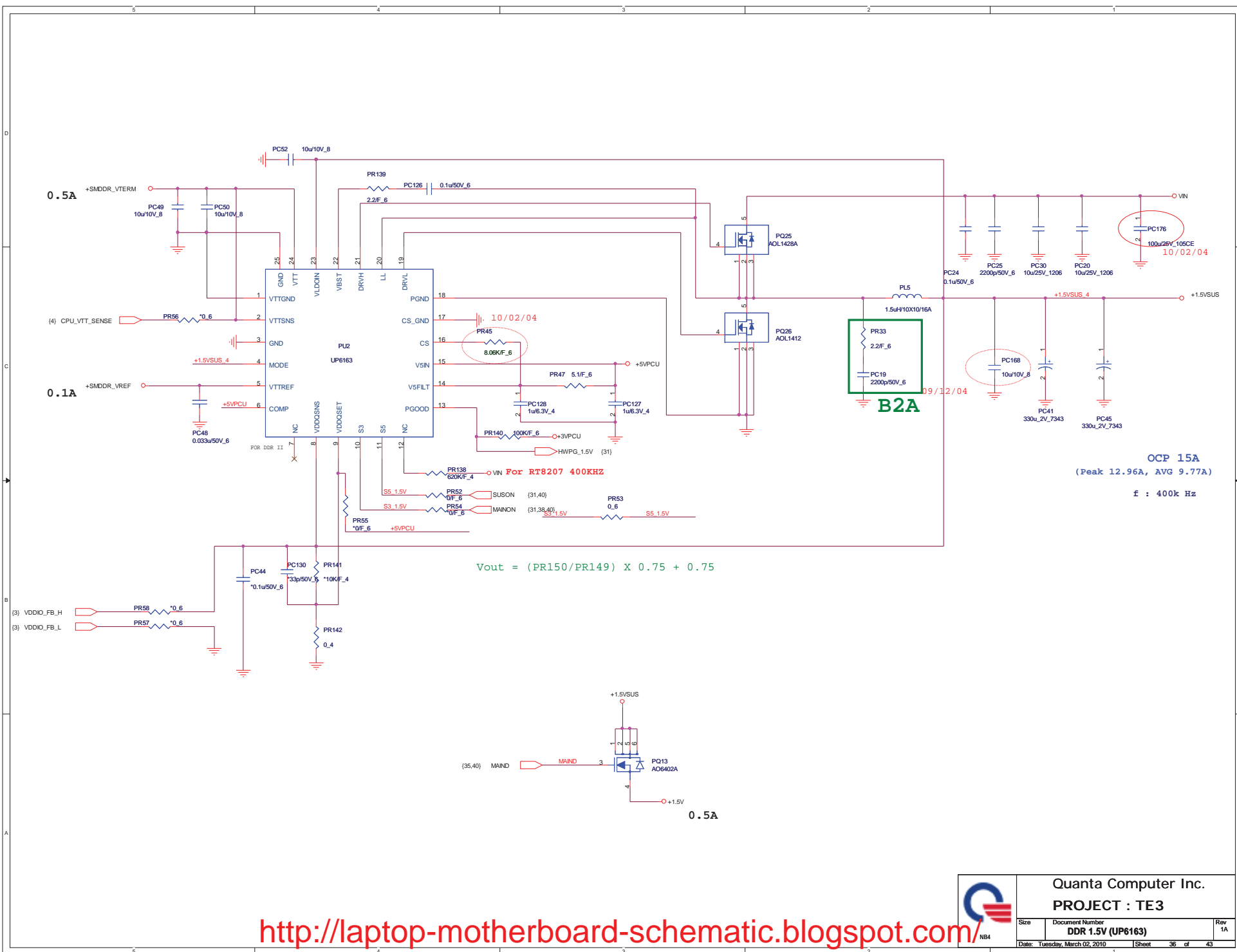
<http://laptop-motherboard-schematic.blogspot.com/>




<http://laptop-motherboard-schematic.blogspot.com/>

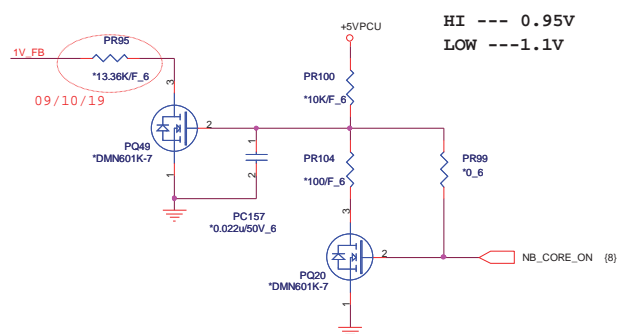
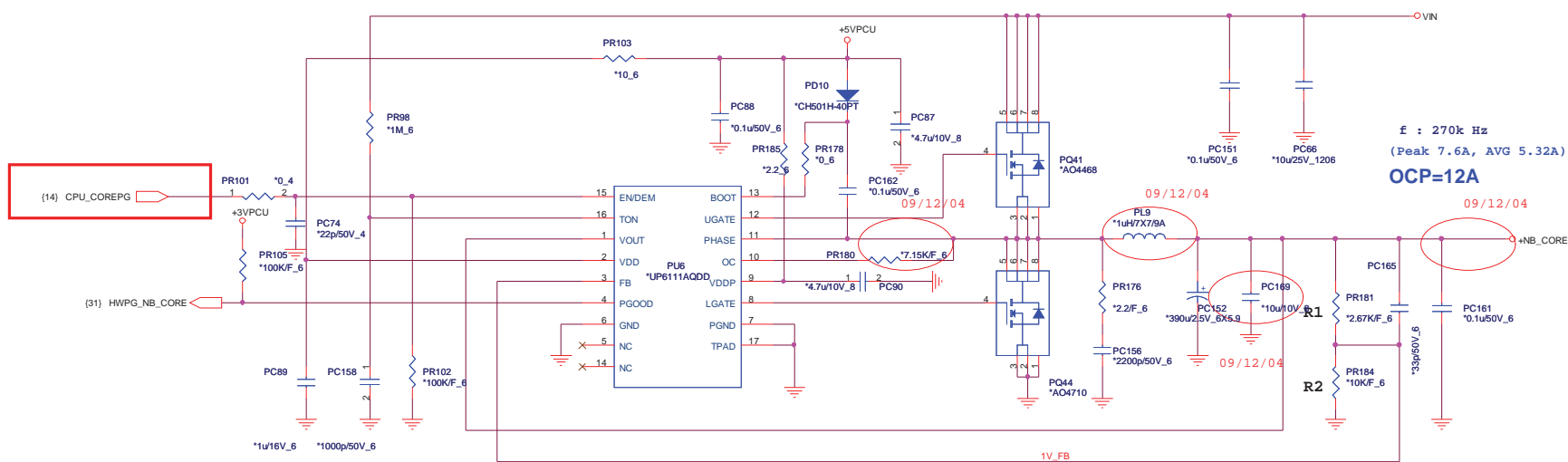


Quanta Computer Inc.			
PROJECT : TE3			
Size	Document Number	System 5V/3V (RT8210B)	
Date: Tuesday, March 02, 2010	Sheet	35 of 43	Rev 1A




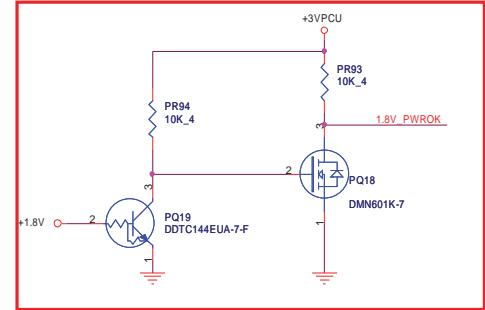
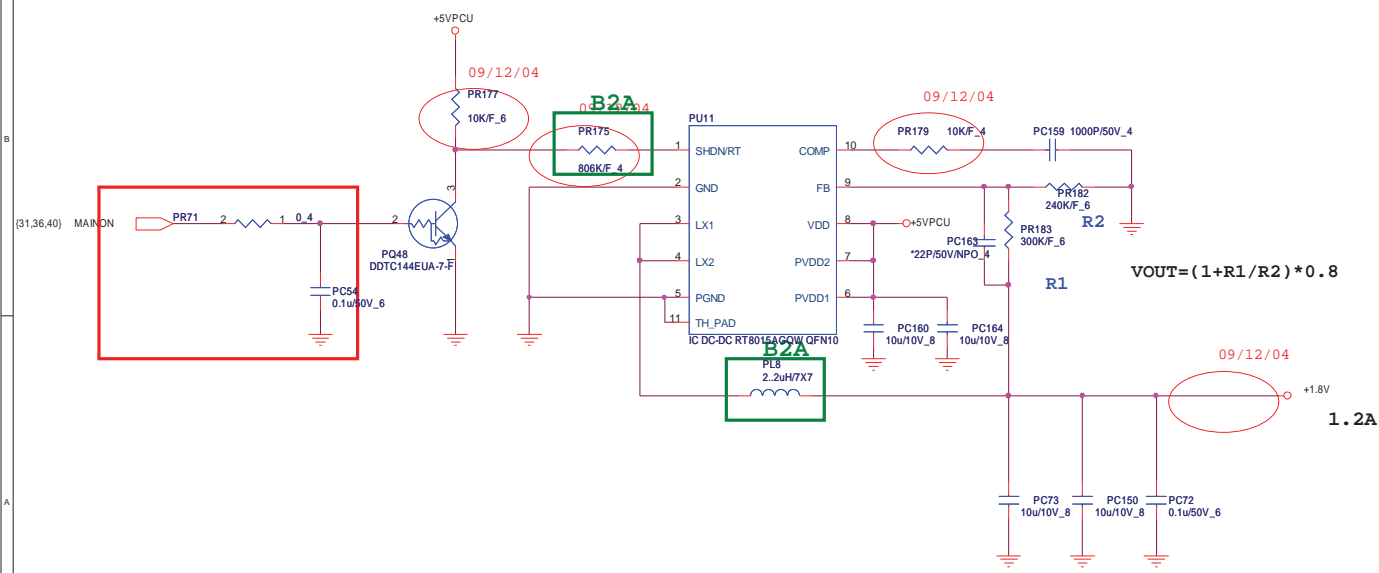
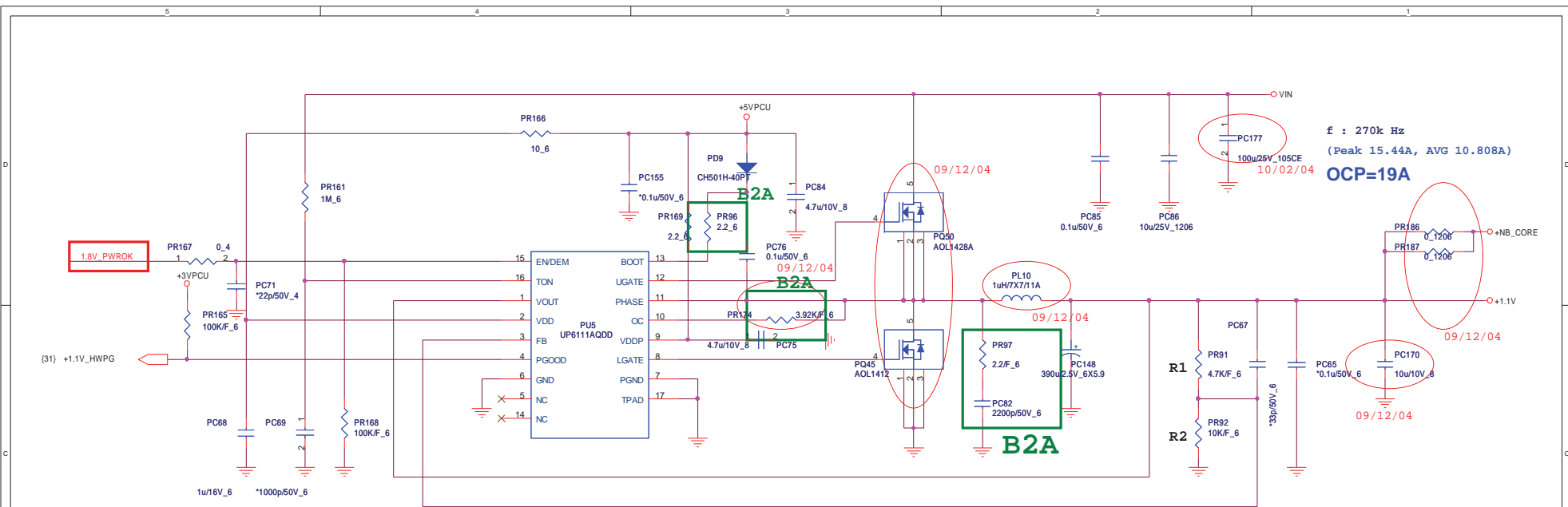
<http://laptop-motherboard-schematic.blogspot.com/>

 Quanta Computer Inc. PROJECT : TE3		
Size	Document Number	Rev
	DDR 1.5V (UP6163)	1A
Date: Tuesday, March 02, 2010	Sheet	36 of 43




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 Quanta Computer Inc. PROJECT : TE3		
Size	Document Number	Rev
	NB_CORE (UP6111)	1A
Date:	Tuesday, March 02, 2010	Sheet 37 of 43



<http://laptop-motherboard-schematic.blogspot.com/>

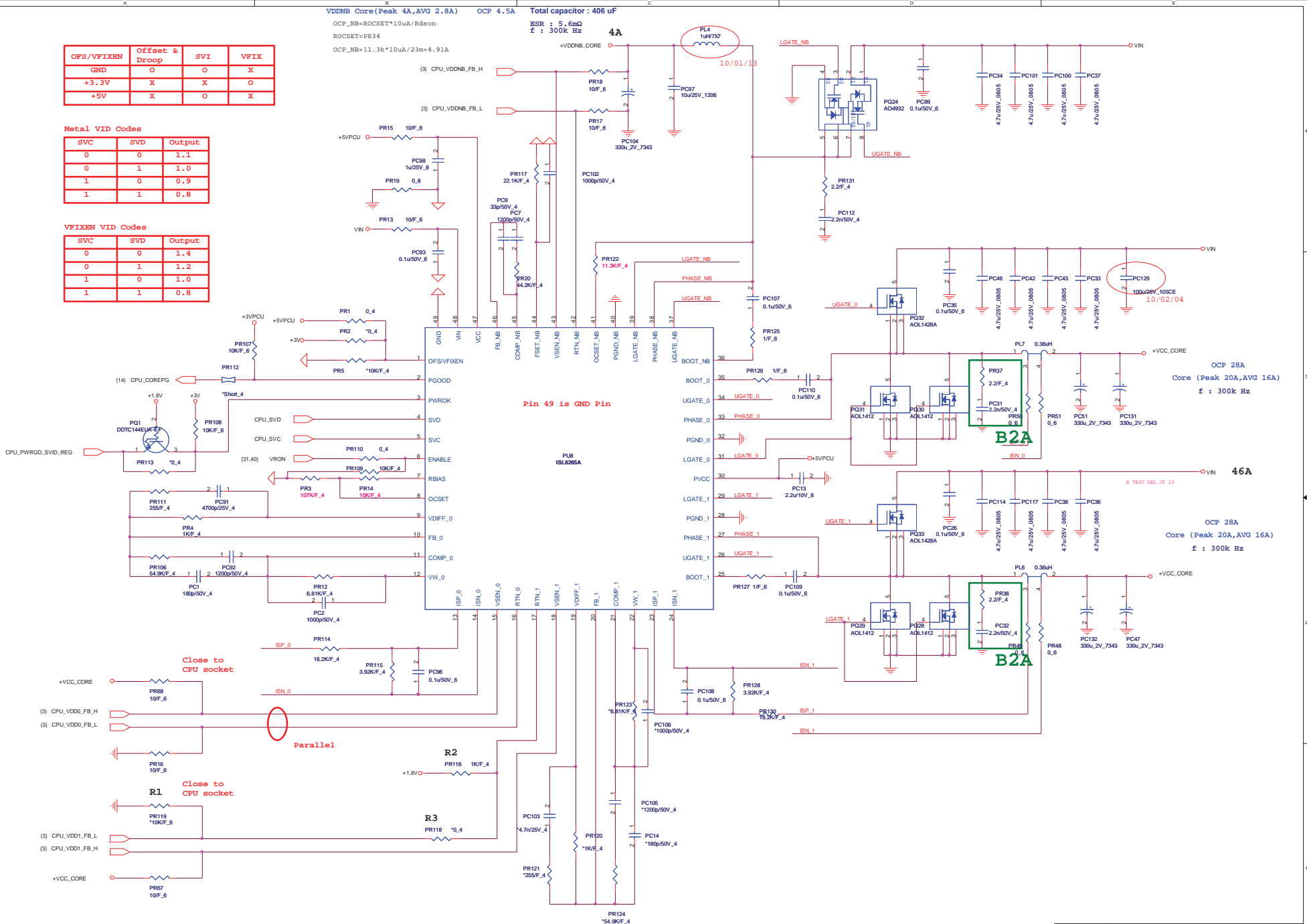
 Quanta Computer Inc. PROJECT : TE3		
Size Custom	Document Number +1.1V/+1.8V (UP6111)	Rev 1A
Date: Tuesday, March 02, 2010	Sheet 38	of 43

OFS/VFIXEN	Offset & Droop	SVI	VFIX
GND	O	O	X
+3.3V	X	X	O
+5V	X	O	X

SVC	SVD	Output
0	0	1.1
0	1	1.0
1	0	0.9
1	1	0.8

SVC	SVD	Output
0	0	1.4
0	1	1.2
1	0	1.0
1	1	0.8

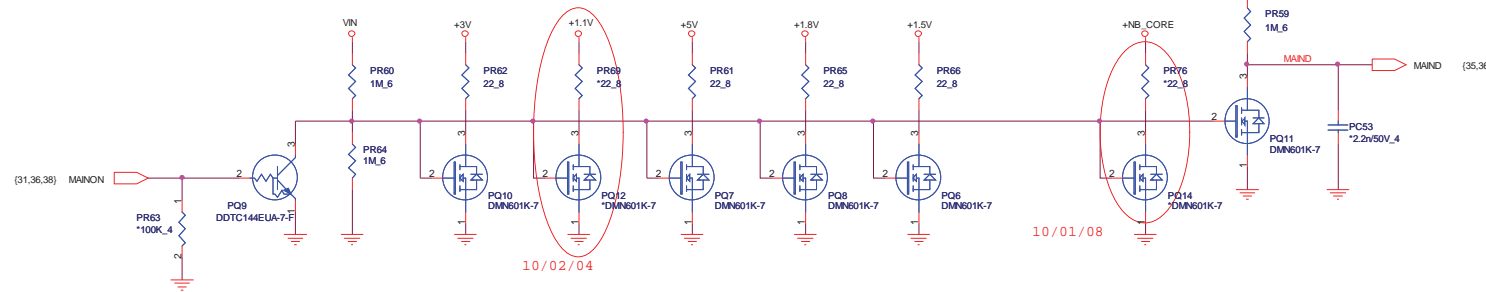
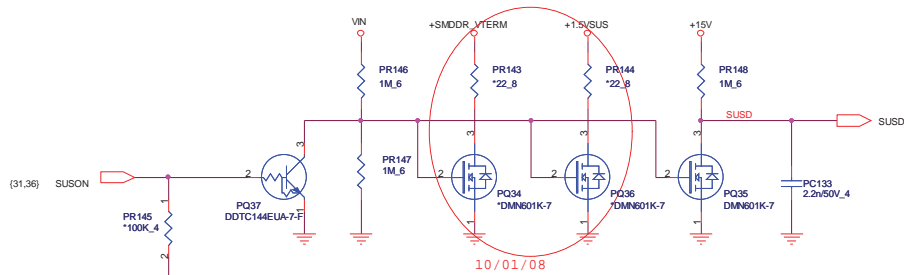
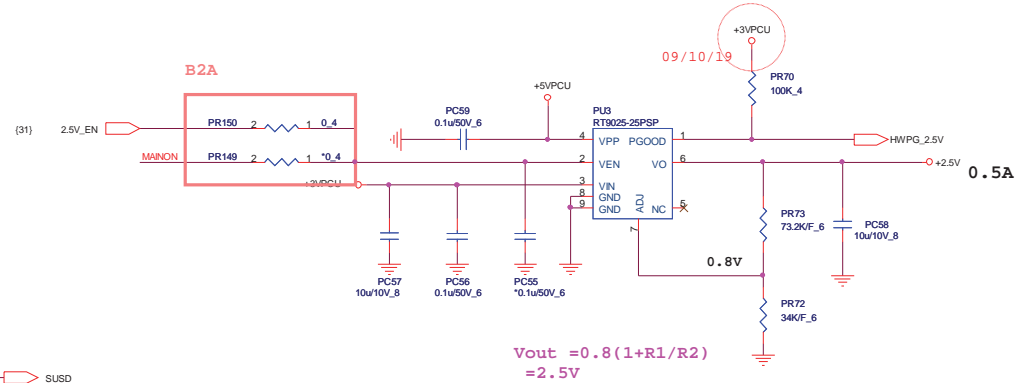
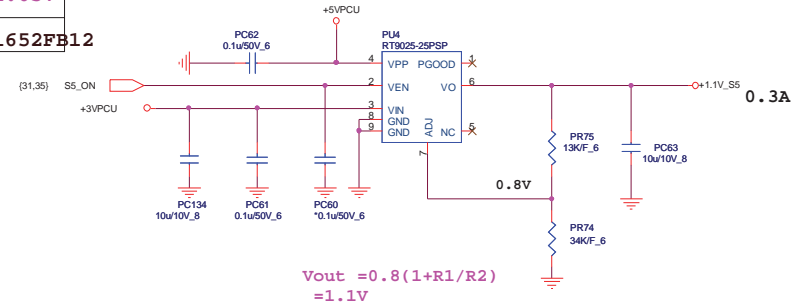
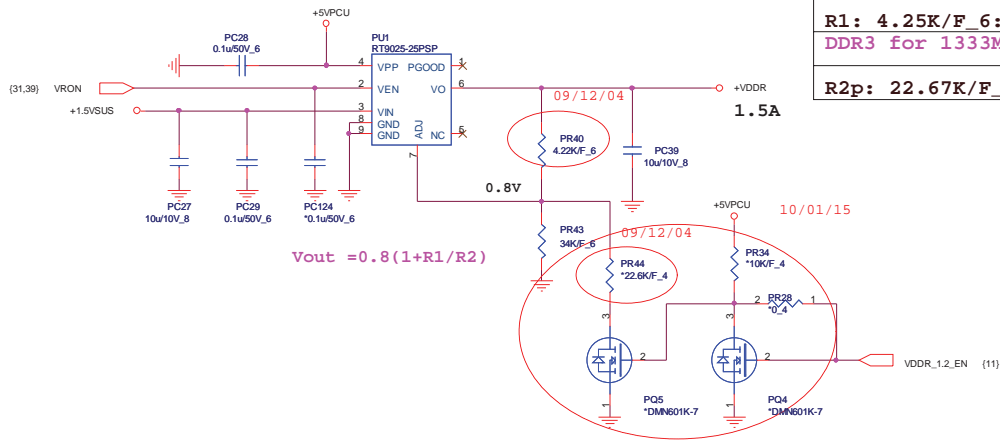
VDDNB Core (Peak 4A, AVG 2.8A) OCP 4.5A Total capacitor : 406 uF
 OCP_NB=ROCKET*10uA/Rdaon
 ROCSET=PR34
 OCP_NB=11.3k*10uA/23m=4.91A
 ESR : 5.6mΩ
 f : 300K Hz




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	Quanta Computer Inc.	
	PROJECT : TE3	
Size	Document Number	Rev
NB4	AMD CPU Core (ISL6265A)	1A
Date:	Tuesday, March 02, 2010	Sheet 39 of 43

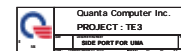
DDR3 for 1066MHZ----0.9V
 R1: 4.25K/F_6: CS31073F908
 DDR3 for 1333MHZ----1.05V
 R2p: 22.67K/F_4: CS31652FB12



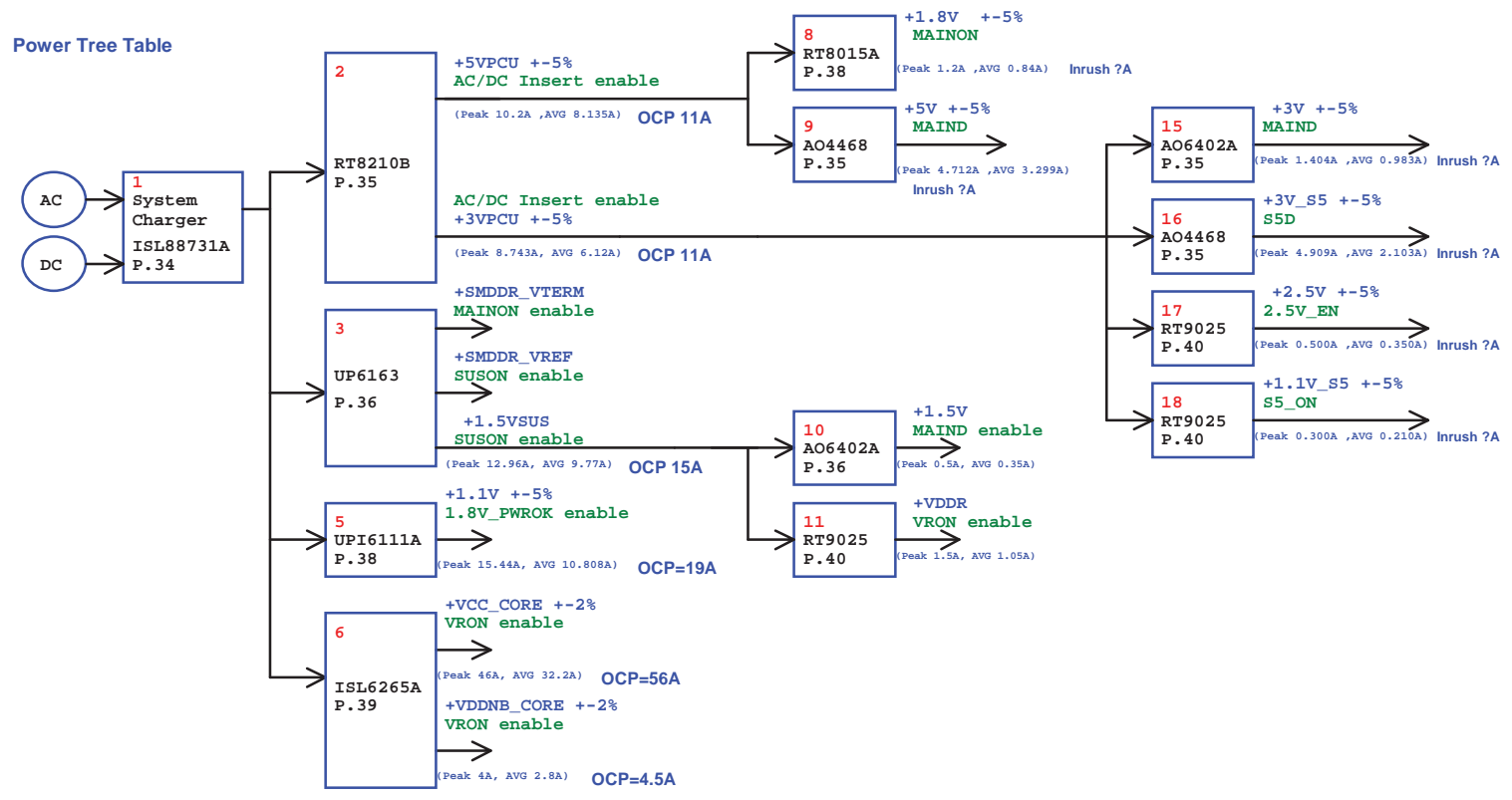
<http://laptop-motherboard-schematic.blogspot.com/>

 Quanta Computer Inc. PROJECT : TE3		
Size	Document Number	Rev
	Discharge/2.5/1.1/VDDR	1A
Date:	Tuesday, March 02, 2010	Sheet 40 of 43

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Power Tree Table




Power Distribution List

Power	Distribution

<http://laptop-motherboard-schematic.blogspot.com/>

Model	REV	CHANGE LIST	MODEL		
			PAGE	FROM	To
TE3 MB	1A	PAGE 2: (CLOCK GEN)--add C209,C218,C212,C3707 FOR PU 3V	1	1A	
		PAGE 12: (SB820M)-- ADD BOARD_ID7,BOARD_ID8	2	1A	
		PAGE 24: (ALL)--CO-LAY L86 Footprint FOR EMI	3	1A	
		PAGE 26: (ALL)--del F2,C564,C567,U20,R448,R449,Q51,C563, USB Swith power change from USBPWR2 to USBPWR1	4	1A	
		PAGE 26: (ALL)--CO-LAY L85,L84 Footprint FOR EMI	5	1A	
		PAGE 29: (ALL)--ADD R465,R729,R636,R637 FOR LAN 52	6	1A	
		PAGE 32: (ALL)--Change CN8 footprint to 88513-0601-6p-l-smt , CN7 footprint to 87213-0600-6P-L	7	1A	
		PAGE 33: (ALL)--change LED seat	8	1A	
		PAGE 39: (ALL)-- change PR174 to 3.92Kohm for OCP setting	9	1A	
		PAGE 39: (ALL)-- change PQ45 footprint for higher current requirement	10	1A	
		PAGE 39: (ALL)-- change PQ50 footprint for higher current requirement	11	1A	
		PAGE 39: (ALL)-- add PC170 for EMI	12	1A	
		2A	PAGE 3: (ALL)--add R33 FOR PU 3V,DEL R32	13	1A
	PAGE 11: (ALL)--DEL U7		14	1A	
	PAGE 23: (ALL)--ADD Q57,Q58,R474,R475 FOR HDMI		15	1A	
	PAGE 28: (ALL)--CHANGE AUDIO IC FROM CX20583 TO CX20587		16	1A	
			17	1A	
			18	1A	
			19	1A	
			20	1A	
			21	1A	
			22	1A	
			23	1A	
			24	1A	
			25	1A	
			26	1A	
			27	1A	
			28	1A	
			29	1A	
		30	1A		

DOC NO. 204	PROJECT MODEL :	TE2A	APPROVED BY:	MAX WENG	DATE:	2009/11/13	 Quanta Computer Inc. PROJECT : TE3
	PART NUMBER:		DRAWING BY:	MAX WENG	REVISION:	1A	
							<small>Rev</small> 1A <small>Date: Tuesday, March 02, 2010 Sheet 43 of 43</small>

<http://laptop-motherboard-schematic.blogspot.com/>